



深圳市富满电子集团股份有限公司

SHEN ZHEN FINE MADE ELECTRONICS GROUP CO., LTD.

TC736 (文件编号: S&CIC1809)

N-Channel Trench Power MOSFET

Description

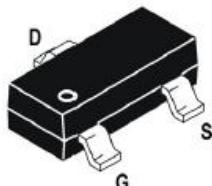
Features

- 20V/6A,
- $R_{DS(ON)} = 12\text{m}\Omega(\text{Typ.}) @ VGS=4.5\text{V}$
- $R_{DS(ON)} = 14\text{m}\Omega(\text{Typ.}) @ VGS=2.5\text{V}$
- Low $R_{DS(ON)}$
- Super High Dense Cell Design
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

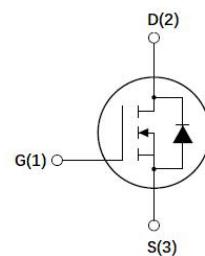
Application

- Power Management
- Load Switch

Package



SOT23-3



N-Channel MOSFET

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Rating	Unit
Common Ratings ($T_A=25^\circ\text{C}$ Unless Otherwise Noted)			
V_{DSS}	Drain-Source Voltage	20	V
V_{GSS}	Gate-Source Voltage	± 12	
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
I_S	Diode Continuous Forward Current	$T_A=25^\circ\text{C}$	1.6
			A
Mounted on Large Heat Sink			
$I_{DP}^{(1)}$	200 μs Pulse Drain Current Tested	$T_A=25^\circ\text{C}$	23
$I_D^{(2)}$	Continuous Drain Current($V_{GS}=4.5\text{V}$)	$T_A=25^\circ\text{C}$	6
		$T_A=70^\circ\text{C}$	4.5
P_D	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	1.27
		$T_A=70^\circ\text{C}$	0.78
R_{JC}	Thermal Resistance-Junction to Case	-	$^\circ\text{C}/\text{W}$
$R_{JA}^{(3)}$	Thermal Resistance-Junction to Ambient	100	$^\circ\text{C}/\text{W}$
Drain-Source Avalanche Ratings			
$E_{AS}^{(4)}$	Avalanche Energy, Single Pulsed	-	mJ



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Electrical Characteristics ($T_c=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	TC736			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{DS}}=250\mu\text{A}$	20			V
I_{DSS}	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=20\text{V}, \text{V}_{\text{GS}}=0\text{V}$			1	μA
		$\text{T}_J=125^\circ\text{C}$			30	
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_{\text{DS}}=250\mu\text{A}$	0.5	0.8	1.2	V
I_{GSS}	Gate Leakage Current	$\text{V}_{\text{GS}}=\pm 10\text{V}, \text{V}_{\text{DS}}=0\text{V}$			± 100	nA
$\text{R}_{\text{DS(ON)}}^{(5)}$	Drain-Source On-state Resistance	$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_{\text{DS}}=6\text{A}$		11	14	mΩ
		$\text{V}_{\text{GS}}=2.5\text{V}, \text{I}_{\text{DS}}=5\text{A}$		14	18	mΩ
Diode Characteristics						
$\text{V}_{\text{SD}}^{(5)}$	Diode Forward Voltage	$\text{I}_{\text{SD}}=1\text{A}, \text{V}_{\text{GS}}=0\text{V}$			1	V
t_{rr}	Reverse Recovery Time	$\text{I}_{\text{SD}}=1\text{A}, \frac{d\text{I}_{\text{SD}}}{dt}=100\text{A}/\mu\text{s}$		16		ns
Q_{rr}	Reverse Recovery Charge			9		nC
Dynamic Characteristics ⁽⁶⁾						
R_G	Gate Resistance	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=0\text{V}, \text{F}=1\text{MHz}$		1.62		Ω
C_{iss}	Input Capacitance	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=10\text{V}, \text{Frequency}=1.0\text{MHz}$		610		pF
C_{oss}	Output Capacitance			127		
C_{rss}	Reverse Transfer Capacitance			93		
$\text{t}_{\text{d(ON)}}$	Turn-on Delay Time	$\text{V}_{\text{DD}}=10\text{V}, \text{I}_{\text{DS}}=6\text{A}, \text{V}_{\text{GEN}}=4.5\text{V}, \text{R}_G=6\Omega$		9		ns
t_{r}	Turn-on Rise Time			17		
$\text{t}_{\text{d(OFF)}}$	Turn-off Delay Time			35		
t_{f}	Turn-off Fall Time			14		
Gate Charge Characteristics ⁽⁶⁾						
Q_g	Total Gate Charge	$\text{V}_{\text{DS}}=16\text{V}, \text{V}_{\text{GS}}=4.5\text{V}, \text{I}_{\text{DS}}=6\text{A}$		12		nC
Q_{gs}	Gate-Source Charge			1.8		
Q_{gd}	Gate-Drain Charge			3.6		

Notes: ①Pulse width limited by safe operating area.

②Calculated continuous current based on maximum allowable junction temperature.

③When mounted on 1 inch square copper board, $t \leq 5\text{sec}$. The value in any given application depends on the user's specific board design.

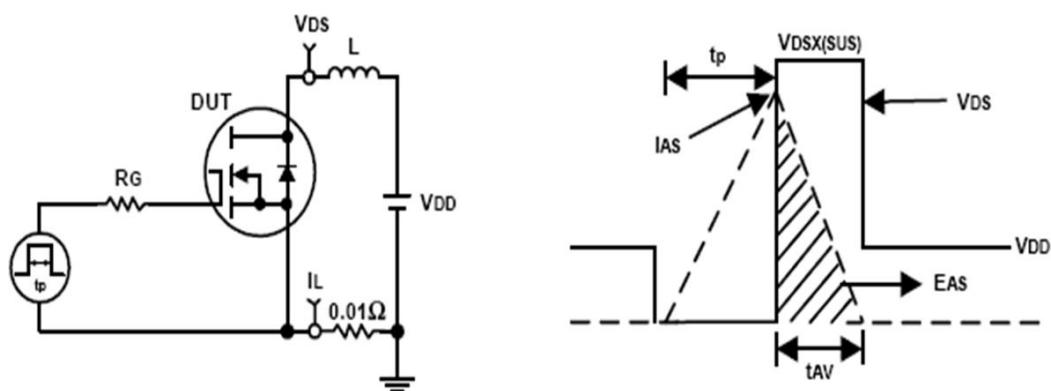
④Limited by T_{Jmax} . Starting $\text{T}_J = 25^\circ\text{C}$.

⑤Pulse test; Pulse width $\leq 200\mu\text{s}$, duty cycle $\leq 2\%$.

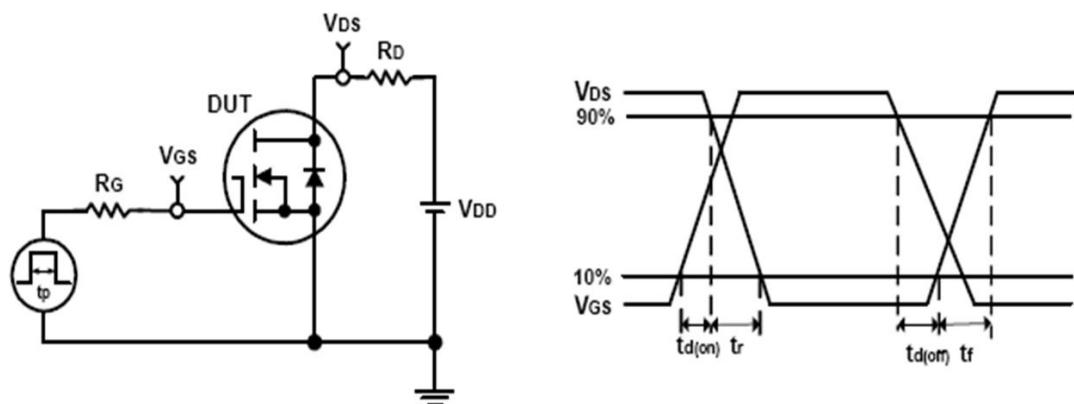
⑥Guaranteed by design, not subject to production testing.



Avalanche Test Circuit and Waveforms



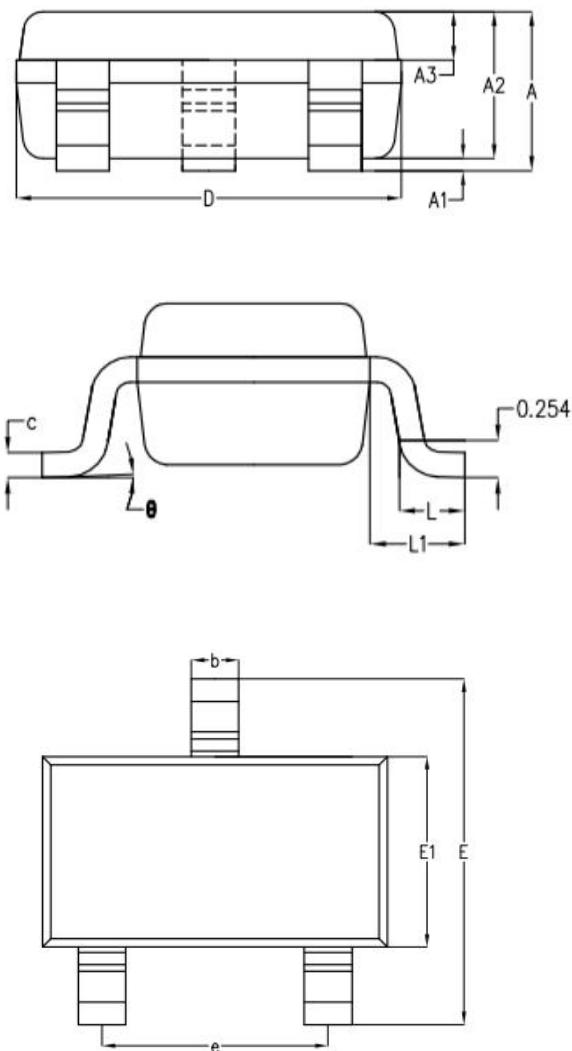
Switching Time Test Circuit and Waveforms





Package Information

- SOT23-3(大)



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	-	1.19	1.24
A1	-	0.05	0.09
A2	1.05	1.10	1.15
A3	0.31	0.36	0.41
b	0.35	0.40	0.45
c	0.12	0.17	0.22
D	2.85	2.90	2.95
E	2.80	2.90	3.00
E1	1.55	1.60	1.65
e	1.90BSC		
L	0.37	0.45	0.53
L1	0.65BSC		
θ	0°	2°	8°