General Description

The AGM60P90D combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$

This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

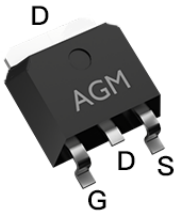
Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
-60V	8.2mΩ	-90A

TO-252 Pin Configuration



Top View

Bottom View

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM60P90D	AGM60P90D	TO-252	330mm	16mm	2500

Table 1. Absolute Maximum Ratings (TA=25℃)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	-60	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25℃) **(Note 1)**	-90	A
	Drain Current-Continuous(Tc=100℃)	-62	A
IDM (pluse)	Drain Current-Pulsed **(Note 2)**	-360	A
PD	Maximum Power Dissipation(Tc=25℃)	125	w
	Maximum Power Dissipation(Tc=100℃)	63	w
EAS	Avalanche energy **(Note 3)**	676	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 175	℃

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State)[1]	---	120	℃/W
RθJC	Thermal Resistance Junction-Case[1]	---	1.2	℃/W

Table 3. Electrical Characteristics (TJ=25℃unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=-250μA	-60	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=-60V,VGS=0V	--	--	-1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=-250μA	-1.2	-1.7	-2.2	V
gFS	Forward Transconductance	VDS=-5V,ID=-15A	--	28	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=-10V, ID=-20A	--	8.2	13	mΩ
		VGS=-4.5V, ID=-15A	--	10	15	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=-30V,VGS=0V, F=1MHZ	--	8005	--	pF
Coss	Output Capacitance		--	296	--	pF
Crss	Reverse Transfer Capacitance		--	265	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	2.2	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=-10V,VDS=-30V, ID=-12A,RGEN=2.5Ω	--	10	--	nS
tr	Turn-on Rise Time		--	60	--	nS
td(off)	Turn-Off Delay Time		--	99	--	nS
tf	Turn-Off Fall Time		--	258	--	nS
Qg	Total Gate Charge	VGS=-10V, VDS=-15V, ID=-12A	--	138	--	nC
Qgs	Gate-Source Charge		--	67	--	nC
Qgd	Gate-Drain Charge		--	132	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	-90	A
VSD	Forward on Voltage	VGS=0V,IS=-20A	--	--	-1.2	V
trr	Reverse Recovery Time	Isd=-20A , dI/dt=100A/μs , TJ=25℃	--	--	--	ns
Qrr	Reverse Recovery Charge		--	--	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25℃,VDD=-30V,Vgs=-10V,ID=-52A, L=0.5mH,RG=25ohm

Typical Characteristics

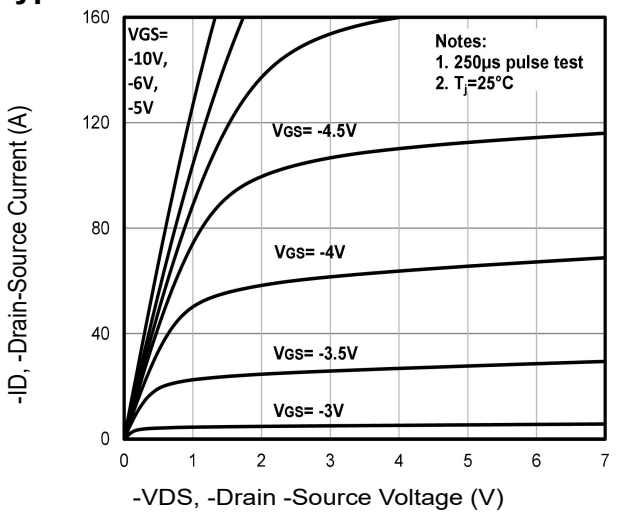


Fig1. Typical Output Characteristics

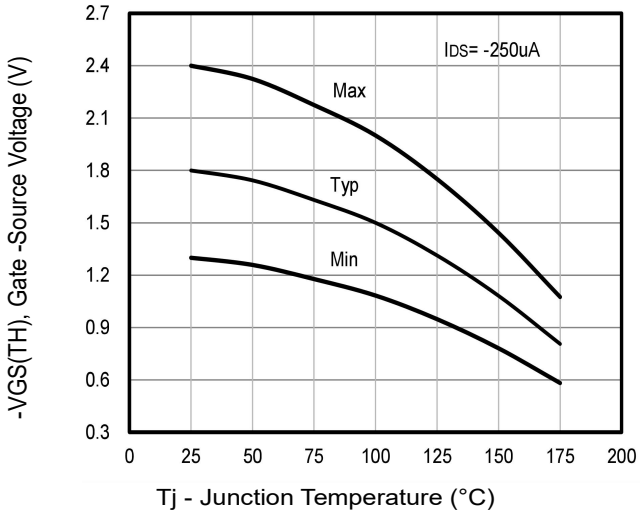


Fig2. Typical $-V_{GS(TH)}$ Gate -Source Voltage Vs. Tj

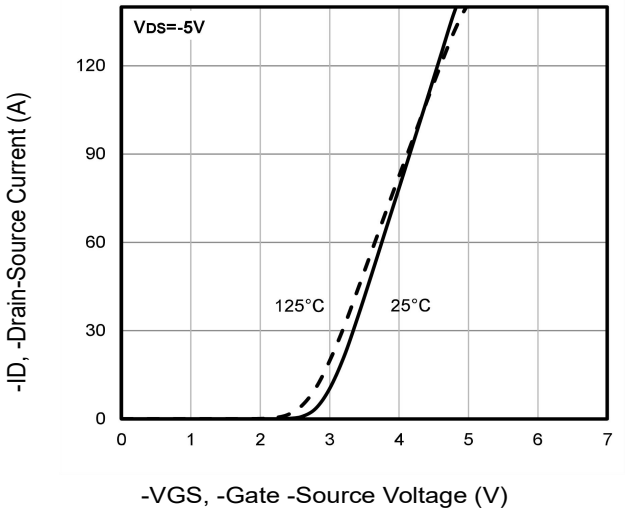


Fig3. Typical Transfer Characteristics

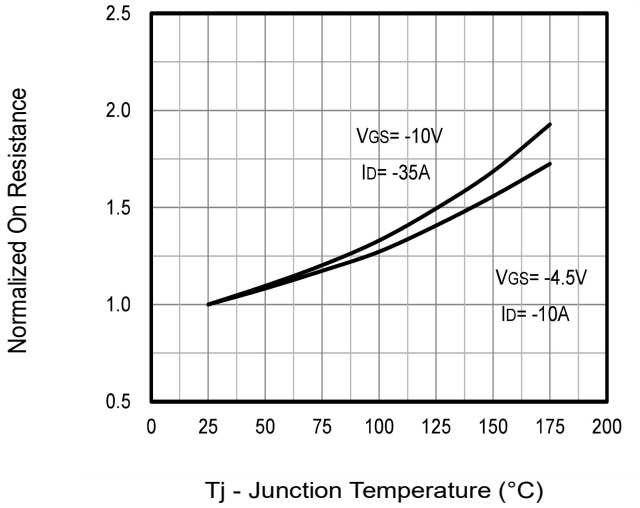


Fig4. Typical Normalized On-Resistance Vs. Tj

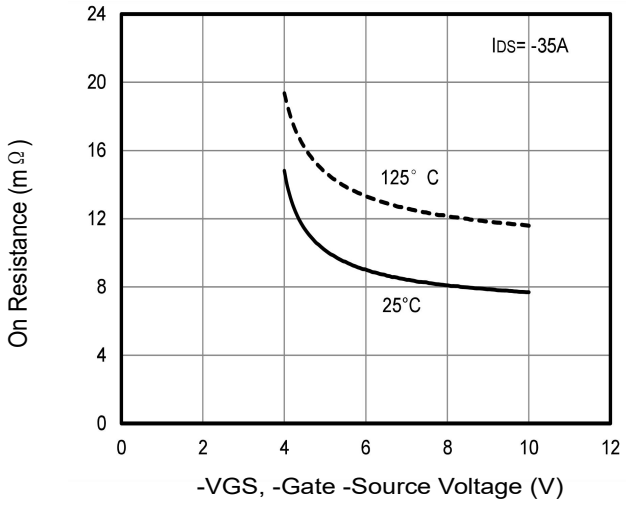


Fig5. Typical On Resistance Vs Gate -Source Voltage

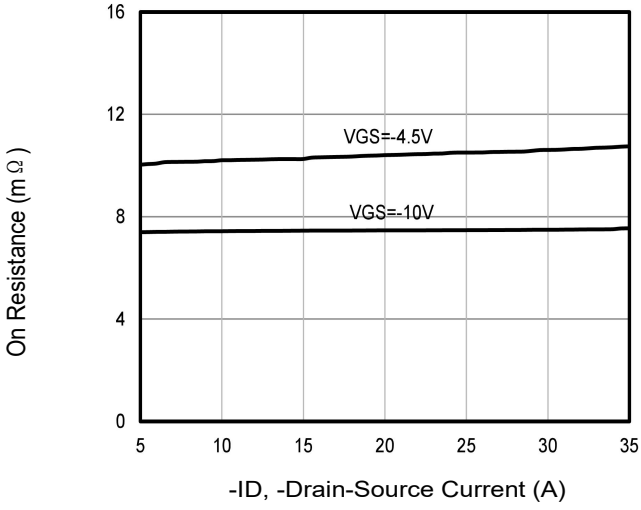


Fig6. Typical On Resistance Vs Drain Current and Gate

Typical Characteristics

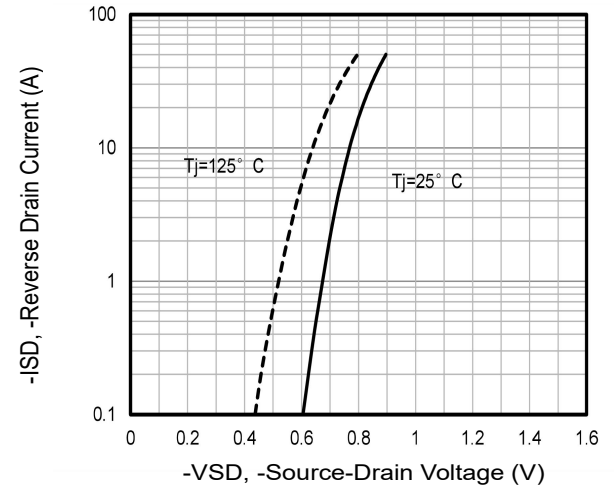


Fig7. Typical Source-Drain Diode Forward Voltage

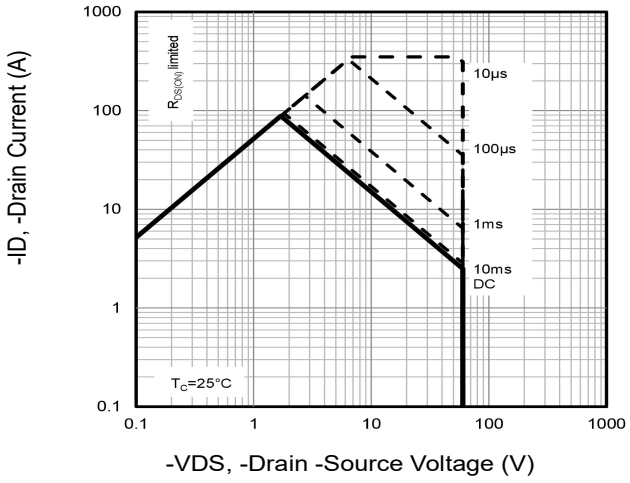


Fig8. Maximum Safe Operating Area

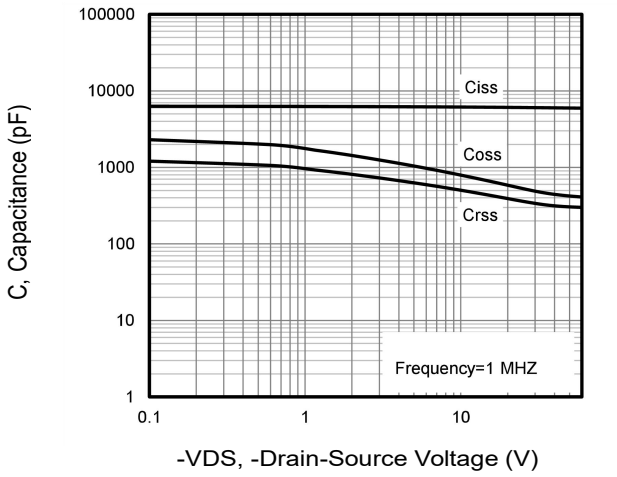


Fig9. Typical Capacitance Vs. Drain-Source Voltage

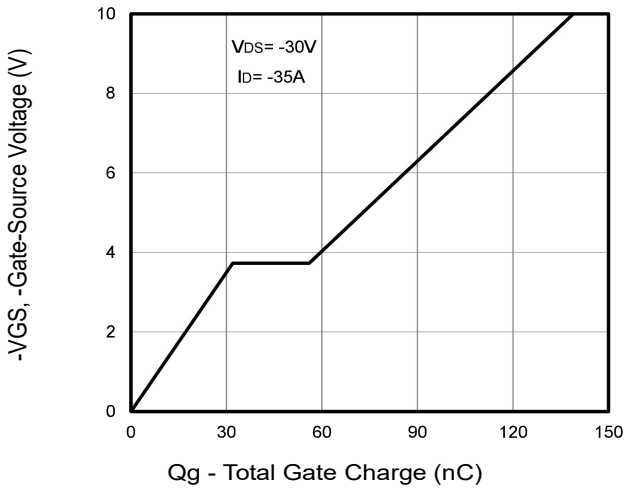


Fig10. Typical Gate Charge Vs. Gate-Source Voltage

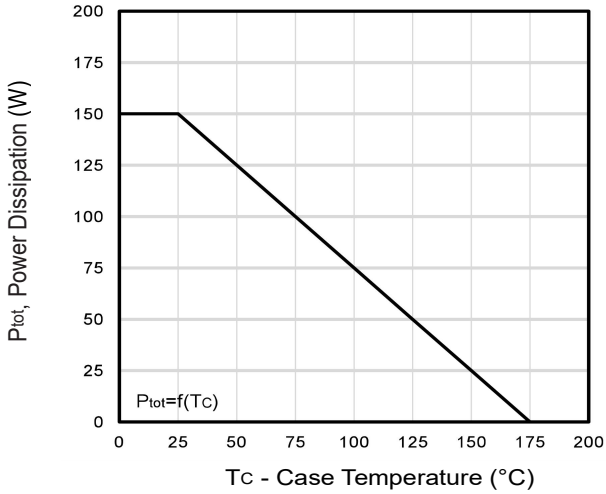


Fig11. Power Dissipation Vs. Case Temperature

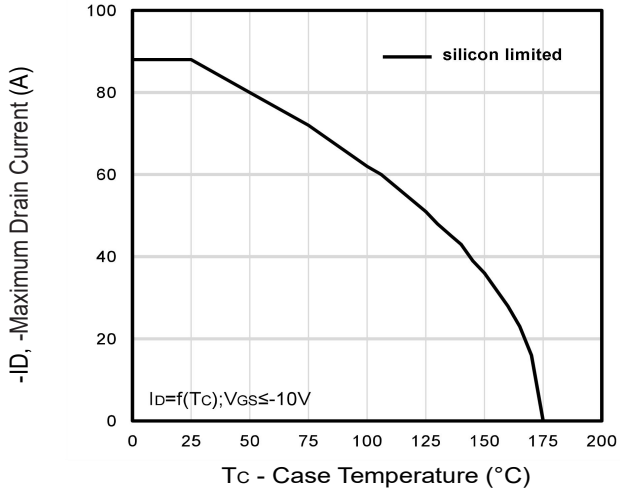


Fig12. Maximum Drain Current Vs. Case Temperature

Typical Characteristics

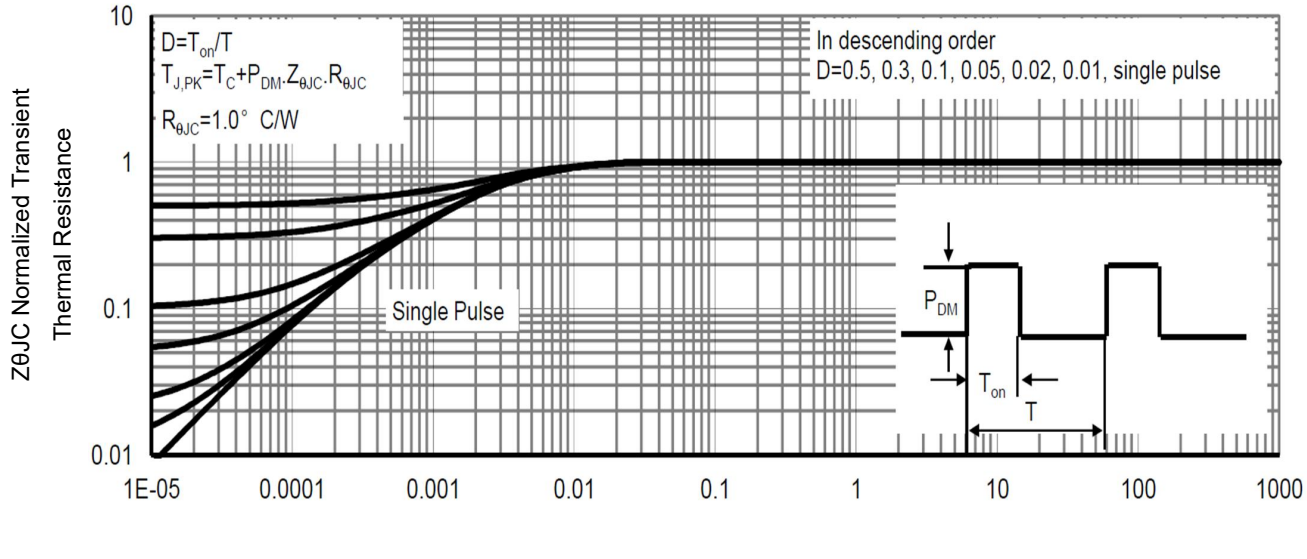


Fig13 . Normalized Maximum Transient Thermal Impedance

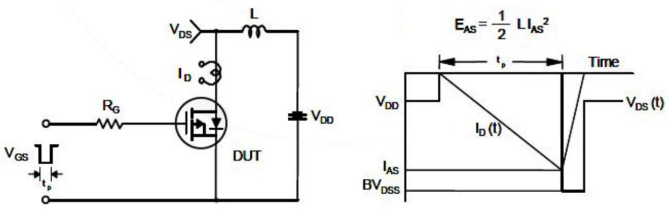


Fig14. Unclamped Inductive Test Circuit and waveforms

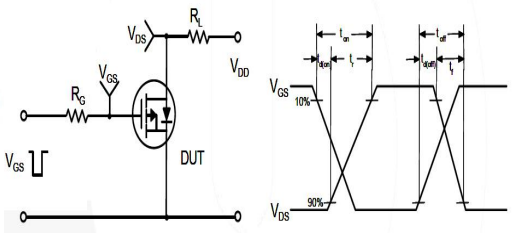
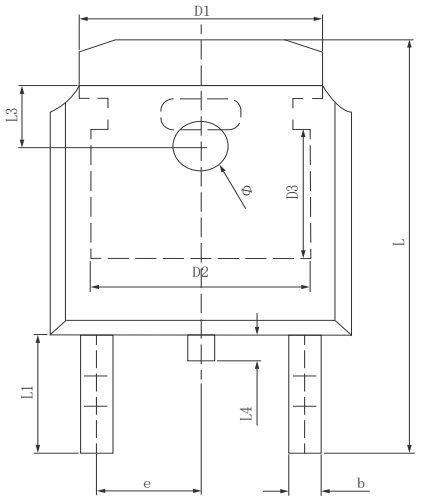
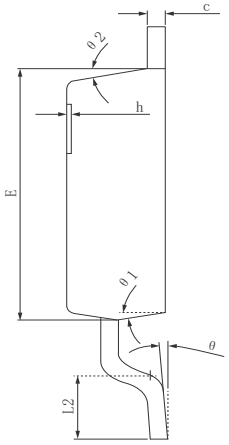


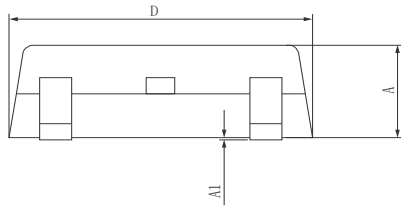
Fig15. Switching Time Test Circuit and waveforms

●Dimensions（TO-252）

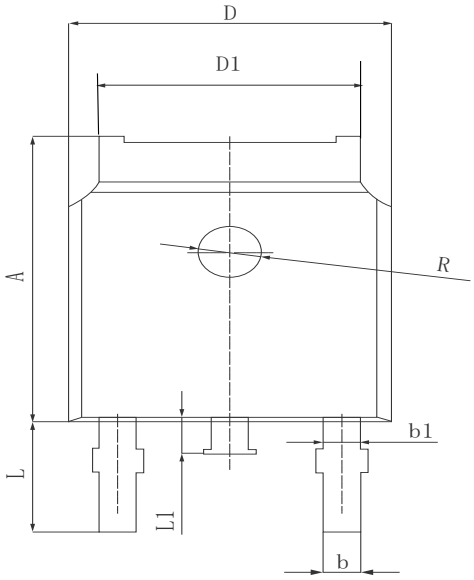


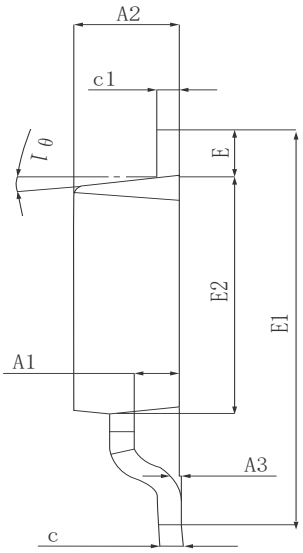


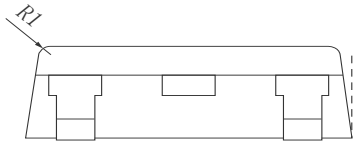
SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
c(电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	4.826 REF		
D3	3.166 REF		
E	6.000	6.100	6.200
e	2.286 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.888 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.600	0.800	1.000
Φ	1.100	1.200	1.300
θ	0°		8°
θ1	9° TYP		
θ2	9° TYP		

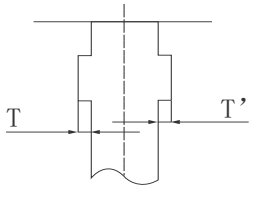


SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	7.050	7.100	7.150
A1	0.960	1.010	1.060
A2	2.250	2.300	2.350
A3	0.000	0.050	0.100
b	0.760REF.		
b1	1.000REF.		
c	0.508REF.		
c1	0.508REF.		
D	6.550	6.600	6.650
D1	5.220	5.320	5.420
E	0.950	1.000	1.050
E1	9.700	9.900	10.100
E2	6.050	6.100	6.150
e	2.286BSC		
e1	4.572REF.		
L	2.650	2.800	2.950
L1	0.700	0.800	0.900
θ1	7° REF.		
R	1.300REF.		
R1	0.250REF.		



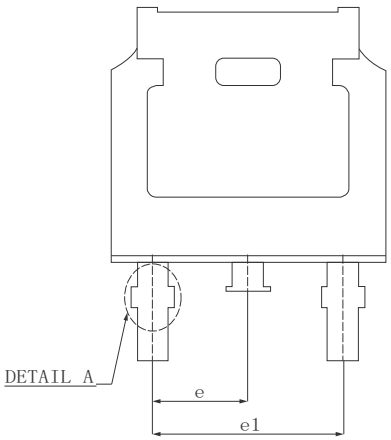




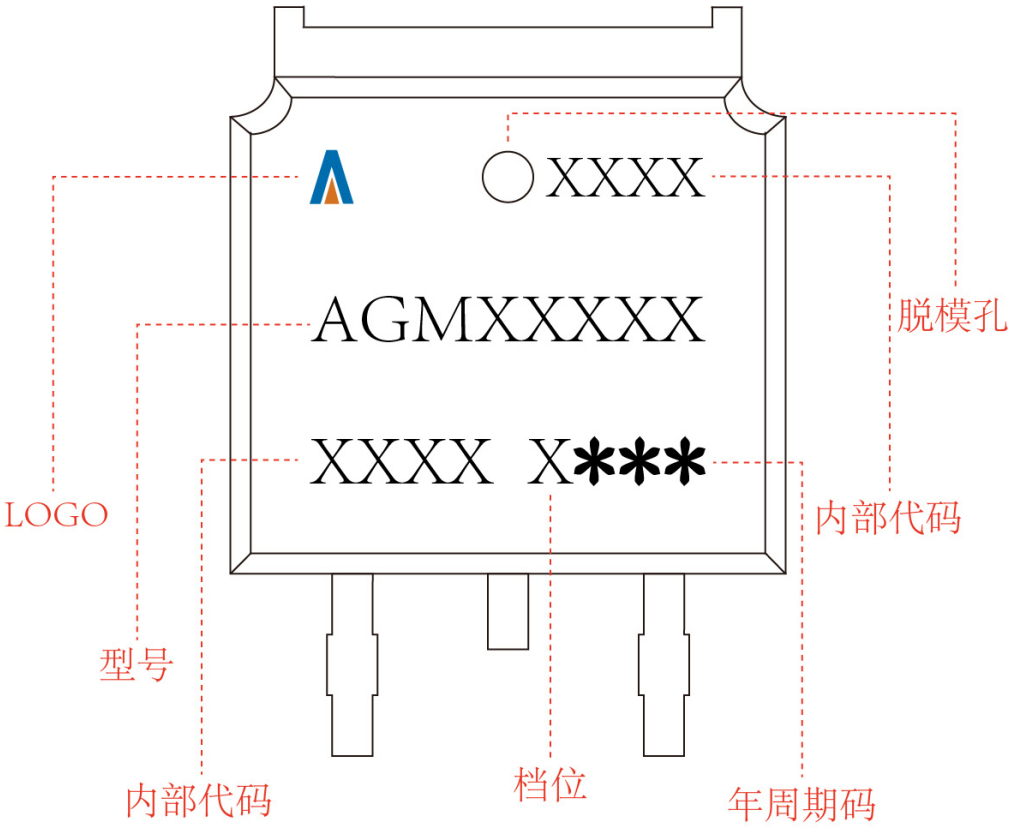


0<=T, T' <=0.12

DETALL A



TO-252
Marking Instructions:



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This document is the fourth version issued on April 10th, 2024. This document replaces all previously provided information.