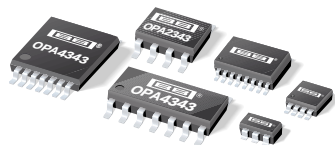




Burr-Brown Products  
from Texas Instruments

OPA343  
OPA2343  
OPA4343



www.ti.com

## SINGLE-SUPPLY, RAIL-TO-RAIL OPERATIONAL AMPLIFIERS

### *microAmplifier™* Series

### FEATURES

- RAIL-TO-RAIL INPUT/OUTPUT
- MICRO SIZE PACKAGES
- WIDE BANDWIDTH: 5.5MHz
- HIGH SLEW RATE: 6V/ $\mu$ s
- LOW THD+NOISE: 0.0007% ( $f = 1\text{kHz}$ )
- LOW QUIESCENT CURRENT: 850 $\mu$ A/chan
- SINGLE, DUAL, AND QUAD VERSIONS

### DESCRIPTION

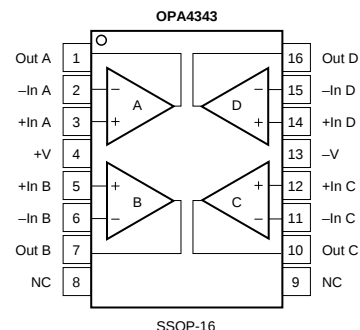
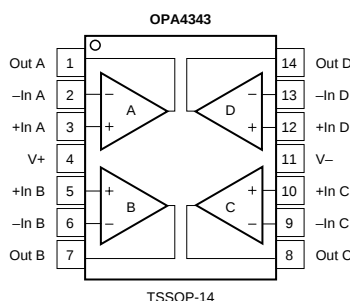
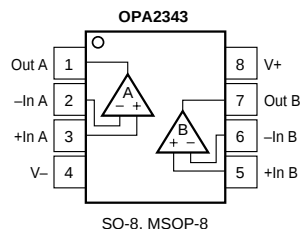
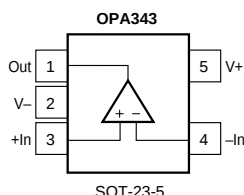
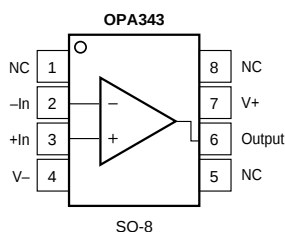
OPA343 series rail-to-rail CMOS operational amplifiers are designed for low-cost, miniature applications. They are optimized for low-voltage, single-supply operation. Rail-to-rail input/output and high-speed operation make them ideal for driving sampling Analog-to-Digital (A/D) converters. They are also well suited for general-purpose and audio applications as well as providing I/V conversion at the output of Digital-to-Analog (D/A) converters. Single, dual, and quad versions have identical specifications for design flexibility.

### APPLICATIONS

- DRIVING A/D CONVERTERS
- PCMCIA CARDS
- DATA ACQUISITION
- AUDIO PROCESSING
- COMMUNICATIONS
- ACTIVE FILTERS
- TEST EQUIPMENT

The OPA343 series operates on a single supply as low as 2.5V, and input common-mode voltage range extends 500mV beyond the supply rails. Output voltage swings to within 1mV of the supply rails with a 100k $\Omega$  load. They offer excellent dynamic response ( $BW = 5.5\text{MHz}$ ,  $SR = 6\text{V}/\mu\text{s}$ ), yet quiescent current is only 850 $\mu$ A. Dual and quad designs feature completely independent circuitry for lowest crosstalk and freedom from interaction.

The single (OPA343) packages are the tiny SOT-23-5 surface mount and SO-8 surface mount. The dual (OPA2343) comes in the miniature MSOP-8 surface mount and SO-8 surface mount. The quad (OPA4343) packages are the space-saving SSOP-16 surface mount, SO-14 surface mount, and TSSOP-14 surface mount. All are specified from  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$  and operate from  $-55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ . A SPICE macromodel is available for design analysis.



# SPECIFICATIONS: $V_S = 2.7V$ to $5.5V$

**Boldface** limits apply over the specified temperature range,  $T_A = -40^{\circ}C$  to  $+85^{\circ}C$ .  $V_S = 5V$ .

At  $T_A = +25^{\circ}C$ ,  $R_L = 10k\Omega$  connected to  $V_S/2$  and  $V_{OUT} = V_S/2$ , unless otherwise noted.

| PARAMETER                                                                                                                                                                                                                                               | CONDITION                                                                                                                                                                                                                                                                                                            | OPA343NA, UA<br>OPA2343EA, UA<br>OPA4343EA, UA, NA                                                                                                          |                                                        |                                                       | UNITS                                                                                                                                           |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------|-------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------|
|                                                                                                                                                                                                                                                         |                                                                                                                                                                                                                                                                                                                      | MIN                                                                                                                                                         | TYP <sup>(1)</sup>                                     | MAX                                                   |                                                                                                                                                 |
| <b>OFFSET VOLTAGE</b><br>Input Offset Voltage<br>vs Temperature<br>vs Power Supply<br><b>Over Temperature</b><br>Channel Separation, dc                                                                                                                 | $V_{OS}$<br>$dV_{OS}/dT$<br>PSRR<br>$V_S = 5V$<br>$V_S = 2.7V$ to $5.5V$ , $V_{CM} = 0V$<br>$V_S = 2.7V$ to $5.5V$ , $V_{CM} = 0V$                                                                                                                                                                                   |                                                                                                                                                             | $\pm 2$<br>$\pm 3$<br>40<br>0.2                        | $\pm 8$<br><br>200<br><b>200</b>                      | mV<br>$\mu V/^{\circ}C$<br>$\mu V/V$<br>$\mu V/V$                                                                                               |
| <b>INPUT BIAS CURRENT</b><br>Input Bias Current<br><b>Over Temperature</b><br>Input Offset Current                                                                                                                                                      | $I_B$<br>$I_{OS}$                                                                                                                                                                                                                                                                                                    |                                                                                                                                                             | $\pm 0.2$<br>$\pm 0.2$                                 | $\pm 10$<br><b><math>\pm 60</math></b><br>$\pm 10$    | pA<br>pA<br>pA                                                                                                                                  |
| <b>NOISE</b><br>Input Voltage Noise, $f = 0.1$ to $50kHz$<br>Input Voltage Noise Density, $f = 1kHz$<br>Current Noise Density, $f = 1kHz$                                                                                                               | $e_n$<br>$i_n$                                                                                                                                                                                                                                                                                                       |                                                                                                                                                             | 8<br>25<br>3                                           |                                                       | $\mu V_{rms}$<br>$nV/\sqrt{Hz}$<br>$fA/\sqrt{Hz}$                                                                                               |
| <b>INPUT VOLTAGE RANGE</b><br>Common-Mode Voltage Range<br>Common-Mode Rejection Ratio                                                                                                                                                                  | $V_{CM}$<br>CMRR<br>$-0.3V < V_{CM} < (V+) - 1.8V$<br>$V_S = 5V$ , $-0.3V < V_{CM} < 5.3V$<br>$V_S = 2.7V$ , $-0.3V < V_{CM} < 3V$                                                                                                                                                                                   | $-0.3$<br>74<br>60<br>54                                                                                                                                    | <br>92<br>75<br>70                                     | $(V+) + 0.3$                                          | V<br>dB<br>dB<br>dB                                                                                                                             |
| <b>INPUT IMPEDANCE</b><br>Differential<br>Common-Mode                                                                                                                                                                                                   |                                                                                                                                                                                                                                                                                                                      |                                                                                                                                                             | $10^{13} \parallel 3$<br>$10^{13} \parallel 6$         |                                                       | $\Omega \parallel pF$<br>$\Omega \parallel pF$                                                                                                  |
| <b>OPEN-LOOP GAIN</b><br>Open-Loop Voltage Gain<br><b>Over Temperature</b><br><br><b>Over Temperature</b><br><br><b>Over Temperature</b>                                                                                                                | $A_{OL}$<br>$R_L = 100k\Omega$ , $5mV < V_O < (V+) - 5mV$<br>$R_L = 100k\Omega$ , $5mV < V_O < (V+) - 5mV$<br>$R_L = 10k\Omega$ , $50mV < V_O < (V+) - 50mV$<br>$R_L = 10k\Omega$ , $50mV < V_O < (V+) - 50mV$<br>$R_L = 2k\Omega$ , $200mV < V_O < (V+) - 200mV$<br>$R_L = 2k\Omega$ , $200mV < V_O < (V+) - 200mV$ | 100<br><b>100</b><br>100<br><b>100</b><br>92<br><b>92</b>                                                                                                   | 120<br><br>117<br><br>110                              |                                                       | dB<br>dB<br>dB<br>dB<br>dB<br>dB                                                                                                                |
| <b>FREQUENCY RESPONSE</b><br>Gain-Bandwidth Product<br>Slew Rate<br>Settling Time, 0.1%<br>0.01%<br>Overload Recovery Time<br>Total Harmonic Distortion + Noise                                                                                         | GBW<br>SR<br>THD+N<br>$V_S = 5V$ , $V_O = 3V_{p-p}^{(2)}$ , $G = 1$ , $f = 1kHz$                                                                                                                                                                                                                                     | $G = 1$<br>$V_S = 5V$ , $G = 1$ , $C_L = 100pF$<br>$V_S = 5V$ , $2V$ Step, $C_L = 100pF$<br>$V_S = 5V$ , $2V$ Step, $C_L = 100pF$<br>$V_{IN} \cdot G = V_S$ | 5.5<br>6<br>1<br>1.6<br>0.2<br>0.0007                  |                                                       | MHz<br>$V/\mu s$<br>$\mu s$<br>$\mu s$<br>$\mu s$<br>%                                                                                          |
| <b>OUTPUT</b><br>Voltage Output Swing from Rail <sup>(3)</sup><br><b>Over Temperature</b><br><br><b>Over Temperature</b><br><br><b>Over Temperature</b><br>Short-Circuit Current<br>Capacitive Load Drive                                               | $I_{SC}$<br>$C_{LOAD}$<br>$R_L = 100k\Omega$ , $A_{OL} \geq 100dB$<br>$R_L = 100k\Omega$ , $A_{OL} \geq 100dB$<br>$R_L = 10k\Omega$ , $A_{OL} \geq 100dB$<br>$R_L = 10k\Omega$ , $A_{OL} \geq 100dB$<br>$R_L = 2k\Omega$ , $A_{OL} \geq 92dB$<br>$R_L = 2k\Omega$ , $A_{OL} \geq 92dB$                               |                                                                                                                                                             | 1<br><br>10<br><br>40<br>$\pm 50$<br>See Typical Curve | 5<br><b>5</b><br>50<br><b>50</b><br>200<br><b>200</b> | mV<br>mV<br>mV<br>mV<br>mV<br>mV<br>mA                                                                                                          |
| <b>POWER SUPPLY</b><br>Specified Voltage Range<br>Operating Voltage Range<br>Quiescent Current (per amplifier)<br><b>Over Temperature</b>                                                                                                               | $V_S$<br>$I_Q$<br>$I_O = 0$ , $V_S = +5V$<br>$I_O = 0$ , $V_S = +5V$                                                                                                                                                                                                                                                 | 2.7                                                                                                                                                         | 2.5 to 5.5<br>0.85                                     | 5<br>1.25<br><b>1.4</b>                               | V<br>V<br>mA<br>mA                                                                                                                              |
| <b>TEMPERATURE RANGE</b><br>Specified Range<br>Operating Range<br>Storage Range<br>Thermal Resistance<br>SOT-23-5 Surface Mount<br>MSOP-8 Surface Mount<br>SO-8 Surface Mount<br>SSOP-16 Surface Mount<br>SO-14 Surface Mount<br>TSSOP-14 Surface Mount | $\theta_{JA}$                                                                                                                                                                                                                                                                                                        | $-40$<br>$-55$<br>$-65$                                                                                                                                     |                                                        | $+85$<br>$+125$<br>$+150$                             | $^{\circ}C$<br>$^{\circ}C$<br>$^{\circ}C$<br>$^{\circ}C/W$<br>$^{\circ}C/W$<br>$^{\circ}C/W$<br>$^{\circ}C/W$<br>$^{\circ}C/W$<br>$^{\circ}C/W$ |

NOTES: (1)  $V_S = +5V$ . (2)  $V_{OUT} = 0.25V$  to  $3.25V$ . (3) Output voltage swings are measured between the output and power supply rails.

## ABSOLUTE MAXIMUM RATINGS<sup>(1)</sup>

|                                                      |                          |
|------------------------------------------------------|--------------------------|
| Supply Voltage .....                                 | 7.5V                     |
| Signal Input Terminals, Voltage <sup>(2)</sup> ..... | (V-) -0.5V to (V+) +0.5V |
| Current <sup>(2)</sup> .....                         | 10mA                     |
| Output Short-Circuit <sup>(3)</sup> .....            | Continuous               |
| Operating Temperature .....                          | -55°C to +125°C          |
| Storage Temperature .....                            | -65°C to +150°C          |
| Junction Temperature .....                           | 150°C                    |
| Lead Temperature (soldering, 10s) .....              | 300°C                    |

NOTES: (1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. (2) Input terminals are diode-clamped to the power supply rails. Input signals that can swing more than 0.5V beyond the supply rails should be current-limited to 10mA or less. (3) Short-circuit to ground, one amplifier per package.



## ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Burr-Brown recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

## PACKAGE/ORDERING INFORMATION

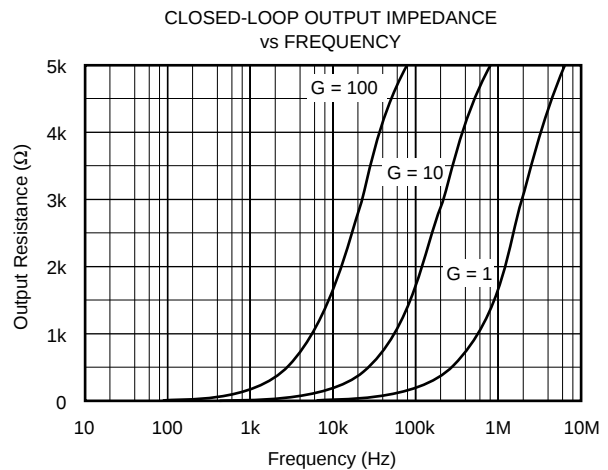
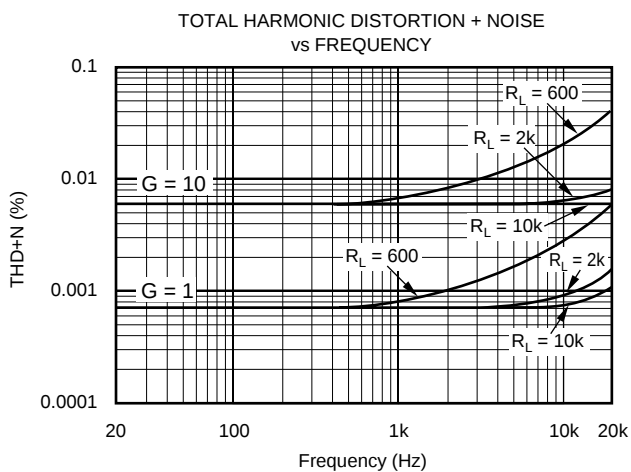
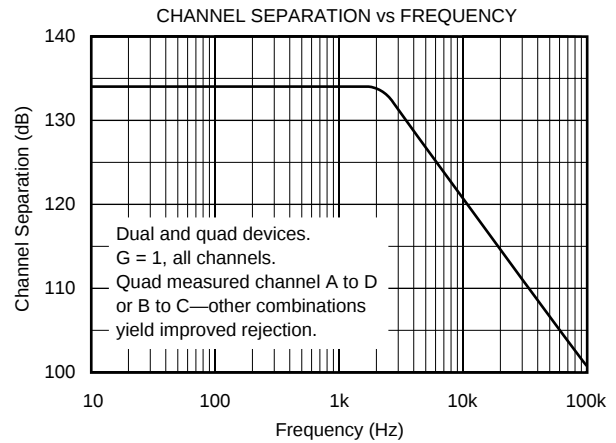
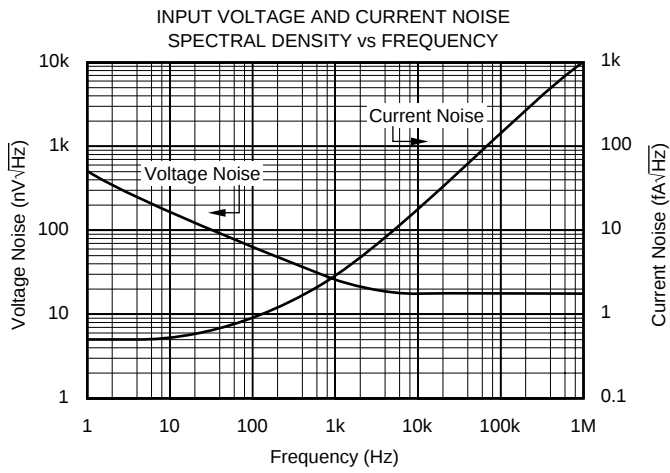
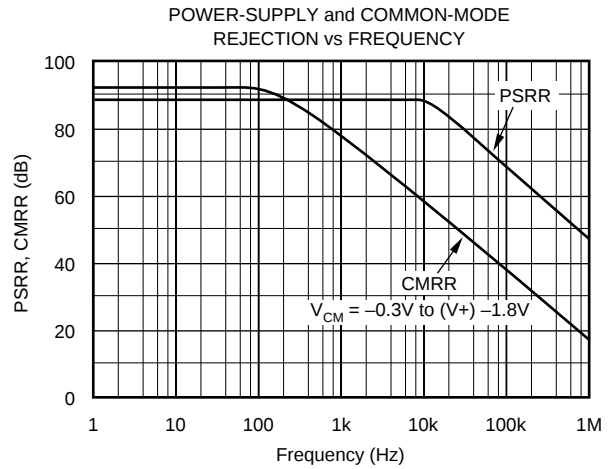
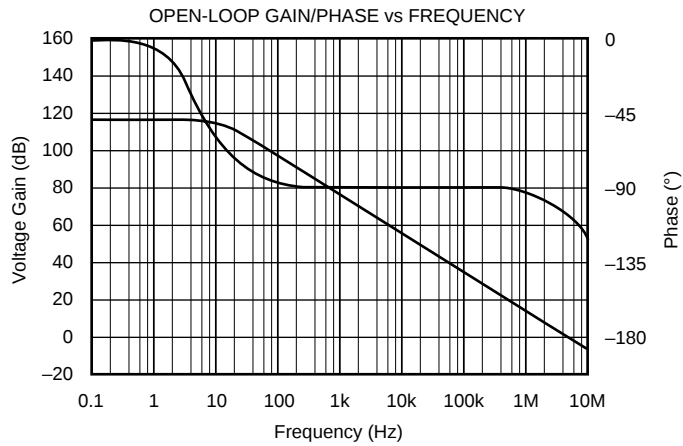
| PRODUCT                          | PACKAGE                       | PACKAGE DRAWING NUMBER | SPECIFIED TEMPERATURE RANGE | PACKAGE MARKING  | ORDERING NUMBER <sup>(1)</sup>                               | TRANSPORT MEDIA                                          |
|----------------------------------|-------------------------------|------------------------|-----------------------------|------------------|--------------------------------------------------------------|----------------------------------------------------------|
| <b>Single</b><br>OPA343NA<br>" " | 5-Lead SOT-23-5<br>" "        | 331<br>" "             | -40°C to +85°C<br>" "       | B43<br>" "       | OPA343NA/250<br>OPA343NA/3K<br>OPA343UA<br>OPA343UA/2K5      | Tape and Reel<br>Tape and Reel<br>Rails<br>Tape and Reel |
| <b>Dual</b><br>OPA2343EA<br>" "  | MSOP-8 Surface-Mount<br>" "   | 337<br>" "             | -40°C to +85°C<br>" "       | C43<br>" "       | OPA2343EA/250<br>OPA2343EA/2K5<br>OPA2343UA<br>OPA2343UA/2K5 | Tape and Reel<br>Tape and Reel<br>Rails<br>Tape and Reel |
| <b>Quad</b><br>OPA4343EA<br>" "  | SSOP-16 Surface-Mount<br>" "  | 322<br>" "             | -40°C to +85°C<br>" "       | OPA4343EA<br>" " | OPA4343EA/250<br>OPA4343EA/2K5<br>OPA4343UA<br>OPA4343UA/2K5 | Tape and Reel<br>Tape and Reel<br>Rails<br>Tape and Reel |
| OPA4343UA<br>" "                 | SO-14 Surface-Mount<br>" "    | 235<br>" "             | -40°C to +85°C<br>" "       | OPA4343UA<br>" " | OPA4343UA/2K5<br>OPA4343NA/250<br>OPA4343NA/2K5              | Tape and Reel<br>Tape and Reel<br>Tape and Reel          |
| OPA4343NA<br>" "                 | TSSOP-14 Surface-Mount<br>" " | 357<br>" "             | -40°C to +85°C<br>" "       | OPA4343NA<br>" " |                                                              |                                                          |

NOTE: (1) Models with a slash (/) are available only in Tape and Reel in the quantities indicated (e.g., /2K5 indicates 2500 devices per reel). Ordering 2500 pieces of "OPA2343EA/2K5" will get a single 2500 piece Tape and Reel.

The information provided herein is believed to be reliable; however, BURR-BROWN assumes no responsibility for inaccuracies or omissions. BURR-BROWN assumes no responsibility for the use of this information, and all use of such information shall be entirely at the user's own risk. Prices and specifications are subject to change without notice. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. BURR-BROWN does not authorize or warrant any BURR-BROWN product for use in life support devices and/or systems.

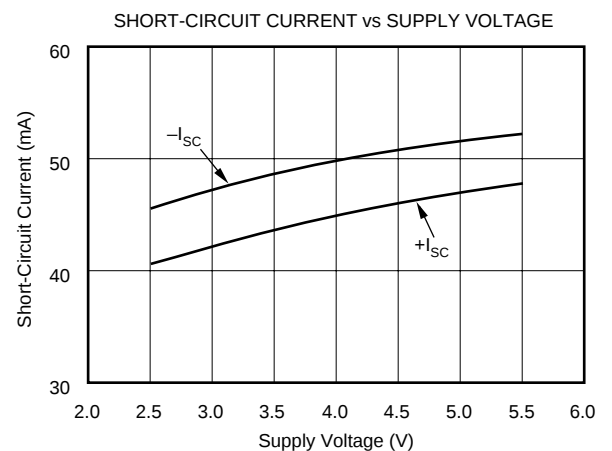
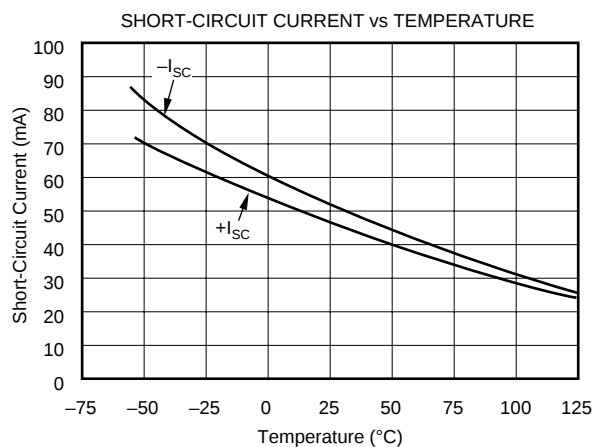
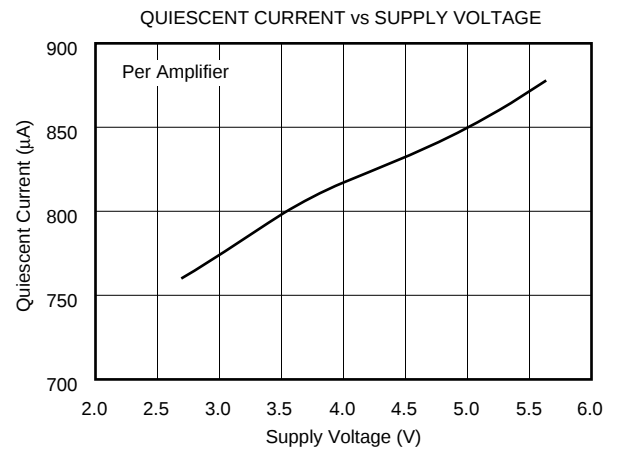
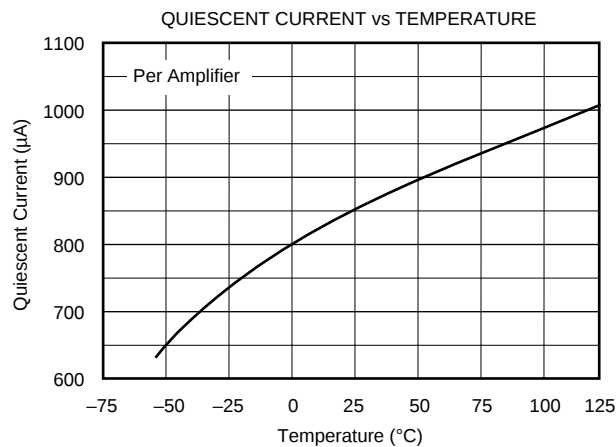
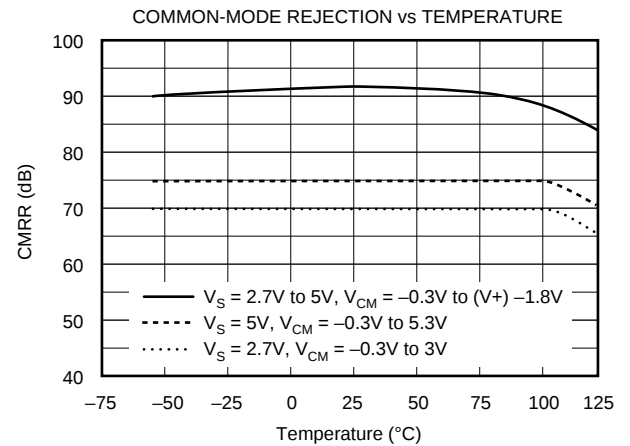
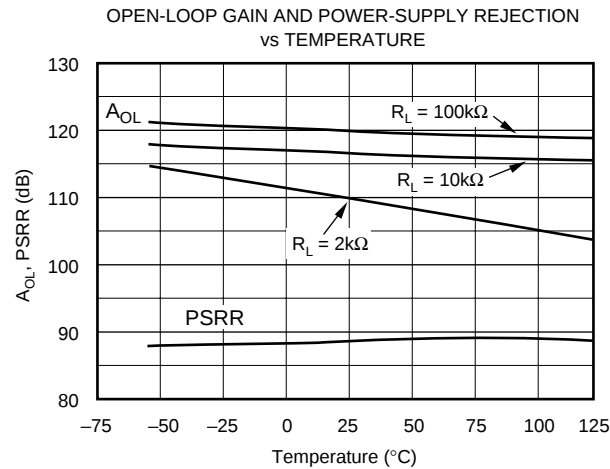
# TYPICAL PERFORMANCE CURVES

At  $T_A = +25^\circ\text{C}$ ,  $V_S = +5\text{V}$ , and  $R_L = 10\text{k}\Omega$  connected to  $V_S/2$ , unless otherwise noted.



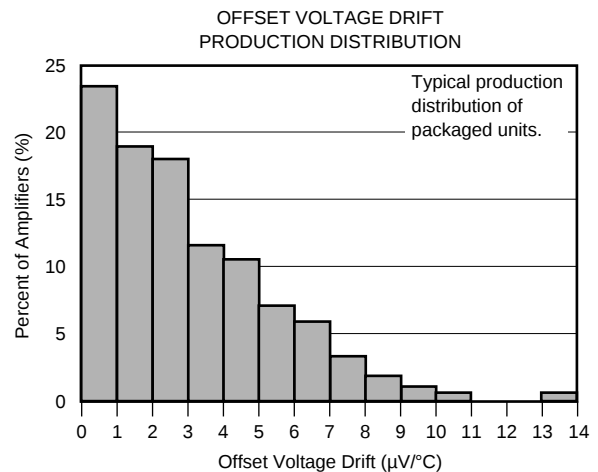
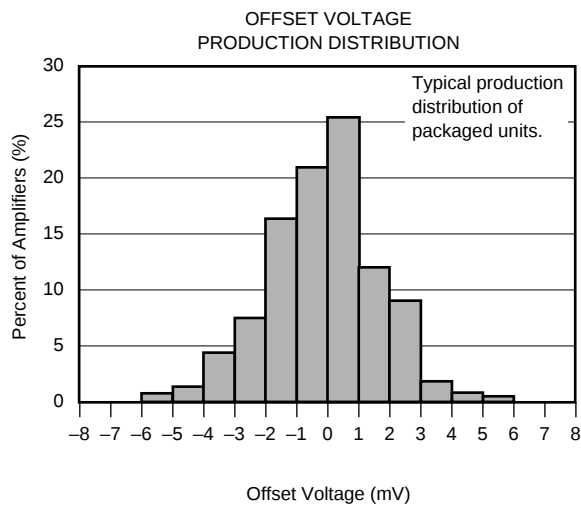
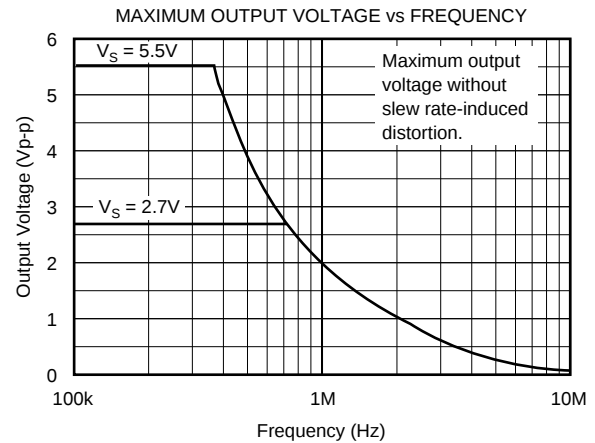
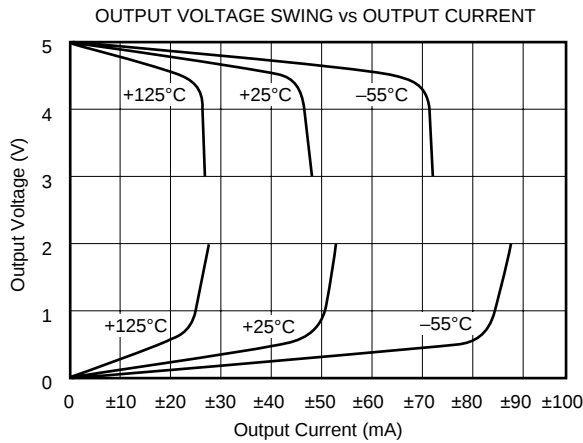
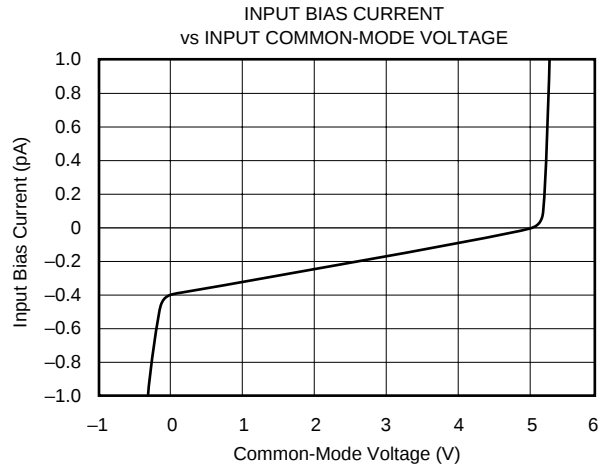
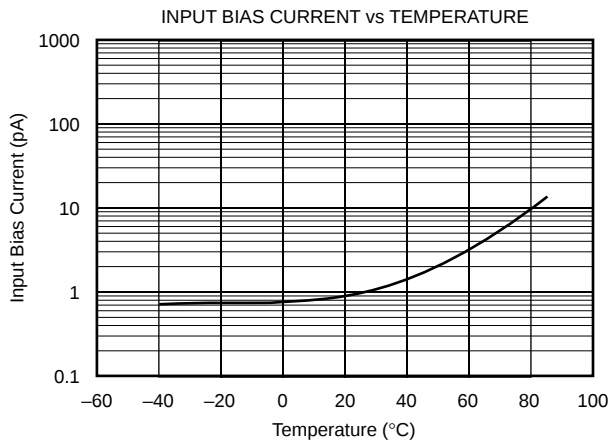
# TYPICAL PERFORMANCE CURVES (Cont.)

At  $T_A = +25^\circ\text{C}$ ,  $V_S = +5\text{V}$ , and  $R_L = 10\text{k}\Omega$  connected to  $V_S/2$ , unless otherwise noted.



# TYPICAL PERFORMANCE CURVES (Cont.)

At  $T_A = +25^\circ\text{C}$ ,  $V_S = +5\text{V}$ , and  $R_L = 10\text{k}\Omega$  connected to  $V_S/2$ , unless otherwise noted.

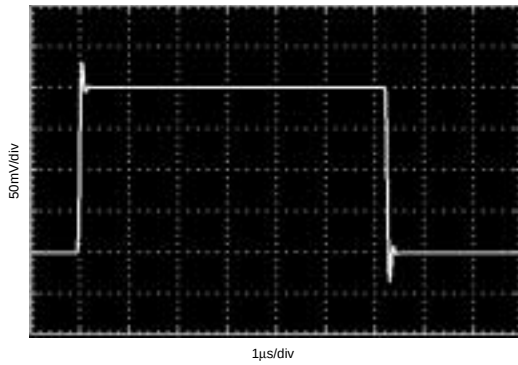


# TYPICAL PERFORMANCE CURVES (Cont.)

At  $T_A = +25^\circ\text{C}$ ,  $V_S = +5\text{V}$ , and  $R_L = 10\text{k}\Omega$  connected to  $V_S/2$ , unless otherwise noted.

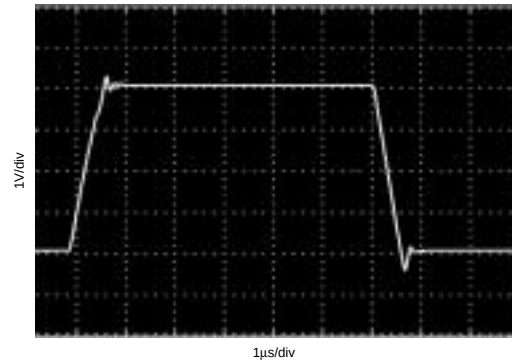
SMALL-SIGNAL STEP RESPONSE

$C_L = 100\text{pF}$

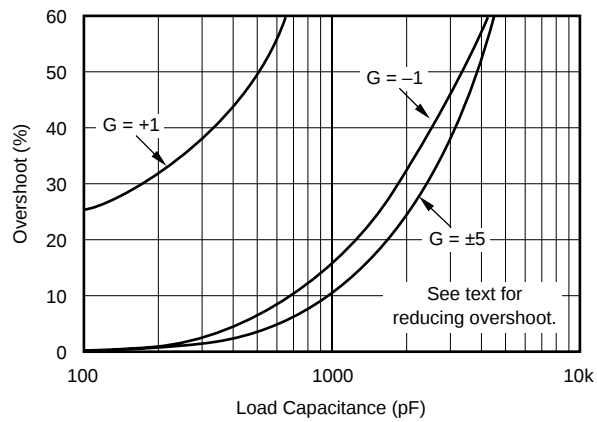


LARGE-SIGNAL STEP RESPONSE

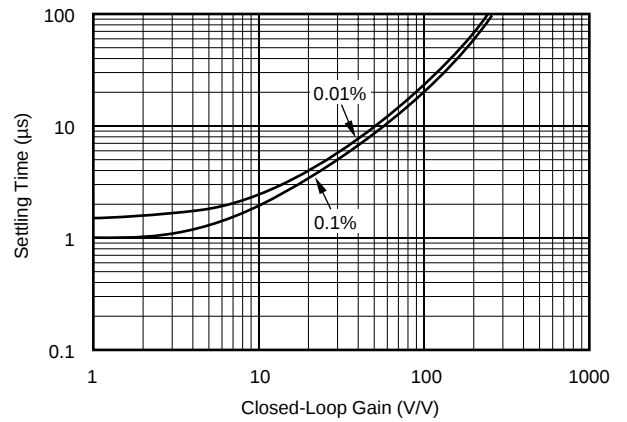
$C_L = 100\text{pF}$



SMALL-SIGNAL OVERSHOOT vs LOAD CAPACITANCE



SETTLING TIME vs CLOSED-LOOP GAIN



## APPLICATIONS INFORMATION

OPA343 series op amps are fabricated on a state-of-the-art 0.6 micron CMOS process. They are unity-gain stable and suitable for a wide range of general-purpose applications. Rail-to-rail input/output make them ideal for driving sampling A/D converters. In addition, excellent ac performance makes them well-suited for audio applications. The class AB output stage is capable of driving  $600\Omega$  loads connected to any point between  $V^+$  and ground.

Rail-to-rail input and output swing significantly increases dynamic range, especially in low-supply applications. Figure 1 shows the input and output waveforms for the OPA343 in unity-gain configuration. Operation is from a single +5V supply with a  $10k\Omega$  load connected to  $V_S/2$ . The input is a 5Vp-p sinusoid. Output voltage is approximately 4.98Vp-p.

Power-supply pins should be bypassed with  $0.01\mu\text{F}$  ceramic capacitors.

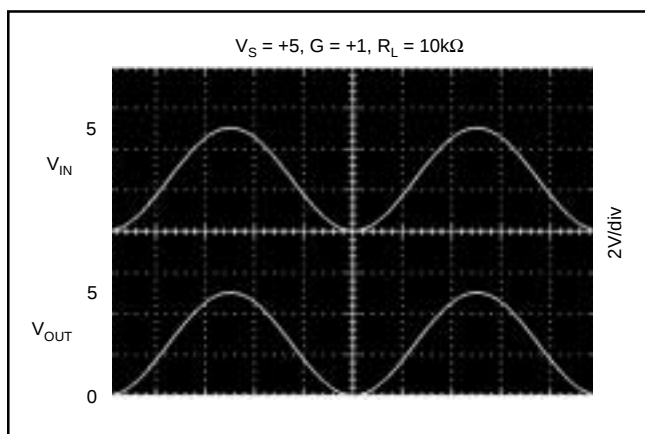


FIGURE 1. Rail-to-Rail Input and Output.

## OPERATING VOLTAGE

OPA343 series op amps are fully specified from +2.7V to +5V. However, supply voltage may range from +2.5V to +5.5V. Parameters are guaranteed over the specified supply range—a unique feature of the OPA343 series. In addition, many specifications apply from  $-40^\circ\text{C}$  to  $+85^\circ\text{C}$ . Most behavior remains virtually unchanged throughout the full operating voltage range. Parameters which vary significantly with operating voltages or temperature are shown in the Typical Performance Curves.

## RAIL-TO-RAIL INPUT

The input common-mode voltage range of the OPA343 series extends 500mV beyond the supply rails. This is achieved with a complementary input stage—an N-channel input differential pair in parallel with a P-channel differential pair, as shown in Figure 2. The N-channel pair is active for input voltages close to the positive rail, typically  $(V^+) - 1.3\text{V}$  to 500mV above the positive supply. The P-channel pair is on for inputs from 500mV below the negative supply to approximately  $(V^+) - 1.3\text{V}$ .

There is a small transition region, typically  $(V^+) - 1.5\text{V}$  to  $(V^+) - 1.1\text{V}$ , in which both input pairs are on. This 400mV transition region can vary  $\pm 300\text{mV}$  with process variation. Thus, the transition region (both stages on) can range from  $(V^+) - 1.8\text{V}$  to  $(V^+) - 1.4\text{V}$  on the low end, up to  $(V^+) - 1.2\text{V}$  to  $(V^+) - 0.8\text{V}$  on the high end. Within the 400mV transition region PSRR, CMRR, offset voltage, offset drift, and THD may be degraded compared to operation outside this region.

A double-folded cascode adds the signal from the two input pairs and presents a differential signal to the class AB output stage. Normally, input bias current is approximately 200fA, however, input voltages exceeding the power supplies by

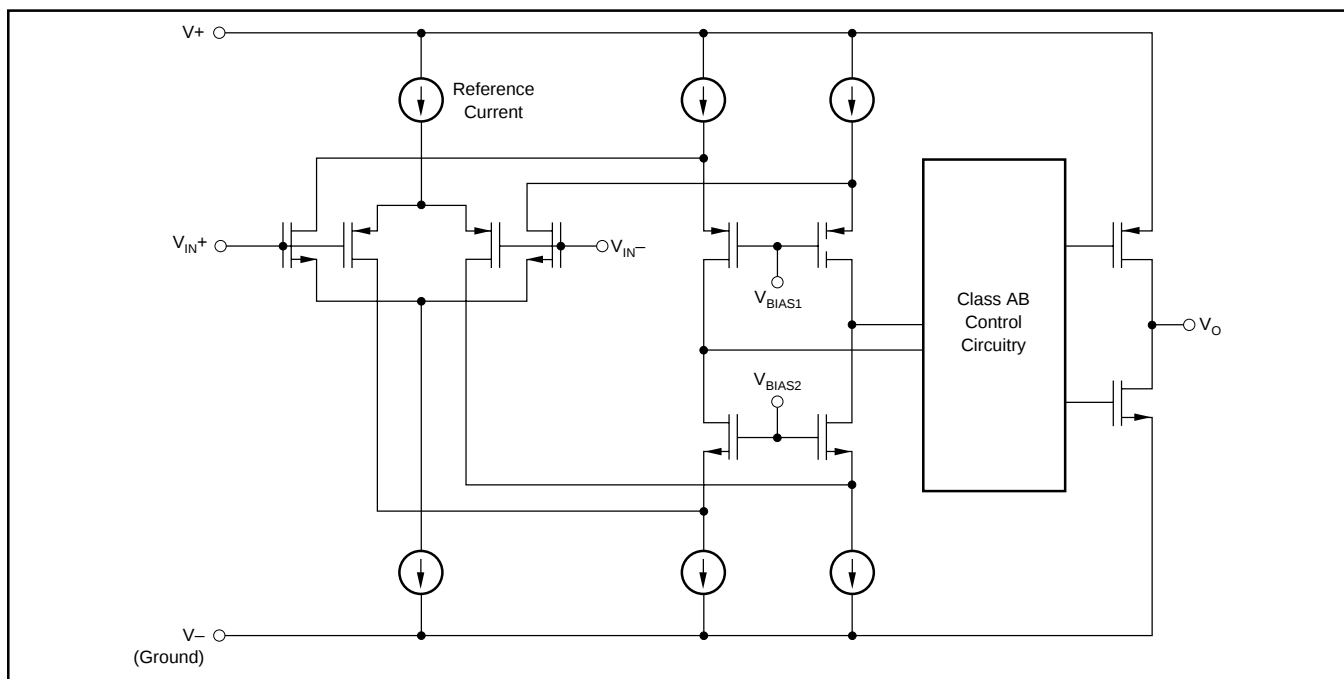


FIGURE 2. Simplified Schematic.

more than 500mV can cause excessive current to flow in or out of the input pins. Momentary voltages greater than 500mV beyond the power supply can be tolerated if the current on the input pins is limited to 10mA. This is easily accomplished with an input resistor, as shown in Figure 3. Many input signals are inherently current-limited to less than 10mA, therefore, a limiting resistor is not required.

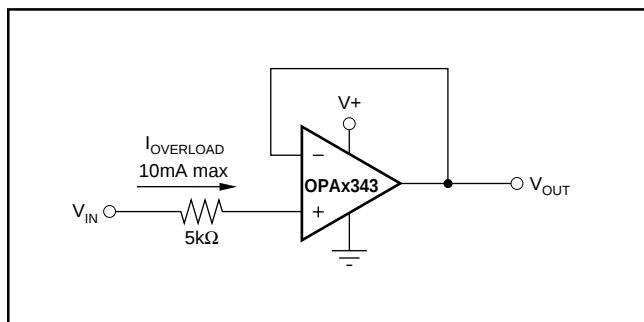


FIGURE 3. Input Current Protection for Voltages Exceeding the Supply Voltage.

### RAIL-TO-RAIL OUTPUT

A class AB output stage with common-source transistors is used to achieve rail-to-rail output. For light resistive loads ( $>50k\Omega$ ), the output voltage is typically a few millivolts from the supply rails. With moderate resistive loads ( $2k\Omega$  to  $50k\Omega$ ), the output can swing to within a few tens of millivolts from the supply rails and maintain high open-loop gain. See the typical performance curve “Output Voltage Swing vs Output Current.”

### CAPACITIVE LOAD AND STABILITY

OPA343 series op amps can drive a wide range of capacitive loads. However, all op amps under certain conditions may become unstable. Op amp configuration, gain, and load value are just a few of the factors to consider when determining stability. An op amp in unity gain configuration is the most susceptible to the effects of capacitive load. The

capacitive load reacts with the op amp’s output resistance, along with any additional load resistance, to create a pole in the small-signal response which degrades the phase margin. In unity gain, OPA343 series op amps perform well, with a pure capacitive load up to approximately 1000pF. Increasing gain enhances the amplifier’s ability to drive more capacitance. See the typical performance curve “Small-Signal Overshoot vs Capacitive Load.”

One method of improving capacitive load drive in the unity gain configuration is to insert a  $10\Omega$  to  $20\Omega$  resistor in series with the output, as shown in Figure 4. This significantly reduces ringing with large capacitive loads. However, if there is a resistive load in parallel with the capacitive load,  $R_S$  creates a voltage divider. This introduces a dc error at the output and slightly reduces output swing. This error may be insignificant. For instance, with  $R_L = 10k\Omega$  and  $R_S = 20\Omega$ , there is only about a 0.2% error at the output.

### DRIVING A/D CONVERTERS

OPA343 series op amps are optimized for driving medium speed (up to 100kHz) sampling A/D converters. However, they also offer excellent performance for higher-speed converters. The OPA343 series provides an effective means of buffering the A/D’s input capacitance and resulting charge injection while providing signal gain. For applications requiring high accuracy, the OPA340 series is recommended.

Figures 5 and 6 show the OPA343 driving an ADS7816. The ADS7816 is a 12-bit, micro-power sampling converter in the tiny MSOP-8 package. When used with the miniature package options of the OPA343 series, the combination is ideal for space-limited and low-power applications. For further information consult the ADS7816 data sheet.

With the OPA343 in a noninverting configuration, an RC network at the amplifier’s output can be used to filter high frequency noise in the signal (see Figure 5). In the inverting configuration, filtering may be accomplished with a capacitor across the feedback resistor (see Figure 6).

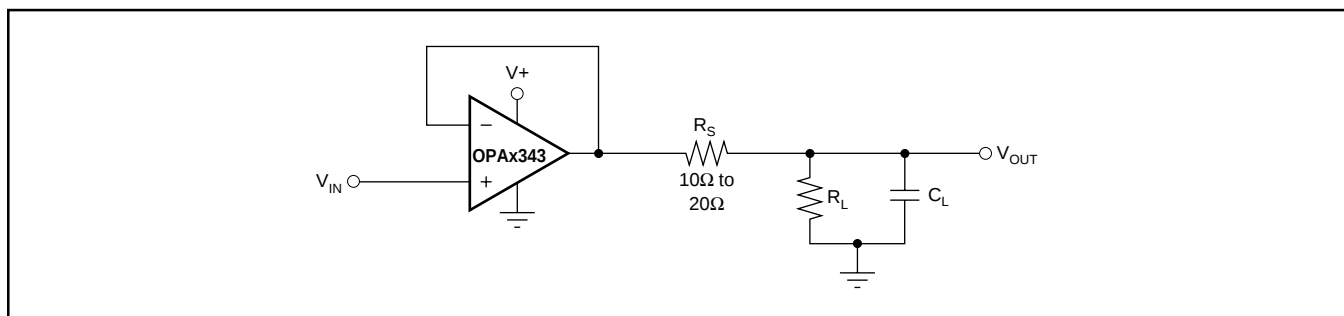


FIGURE 4. Series Resistor in Unity-Gain Configuration Improves Capacitive Load Drive.

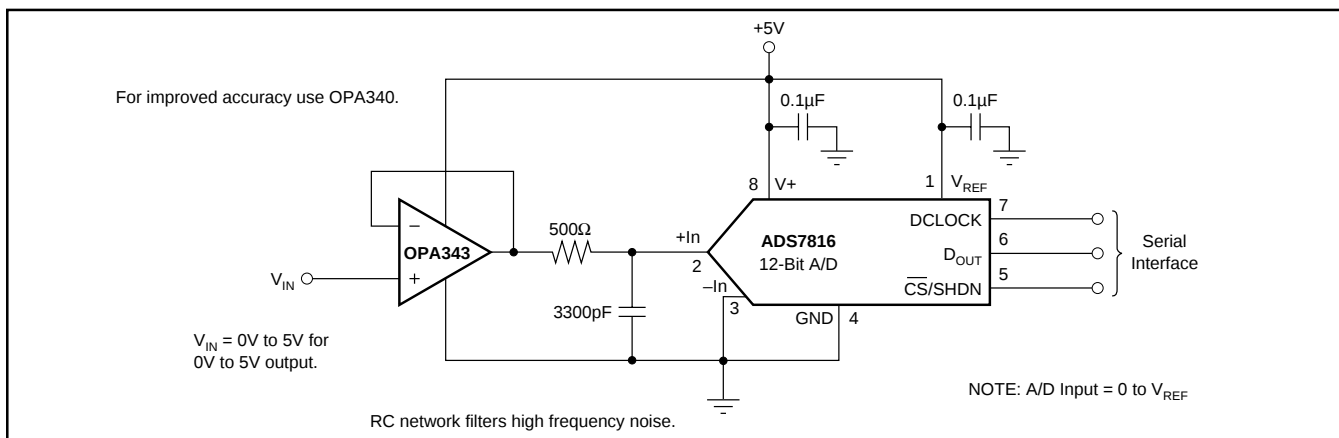


FIGURE 5. OPA343 in Noninverting Configuration Driving ADS7816.

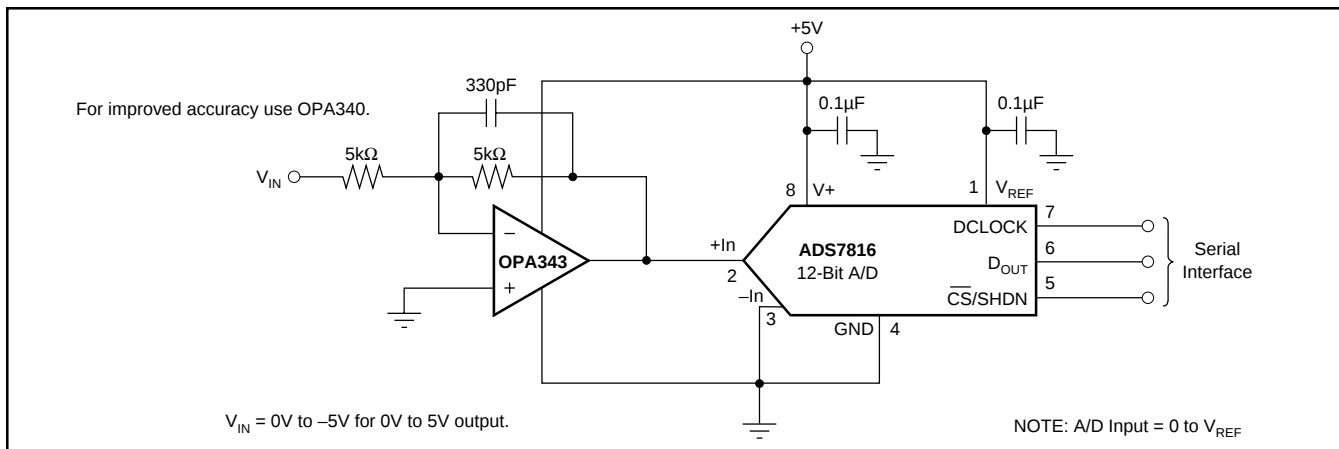


FIGURE 6. OPA343 in Inverting Configuration Driving ADS7816.

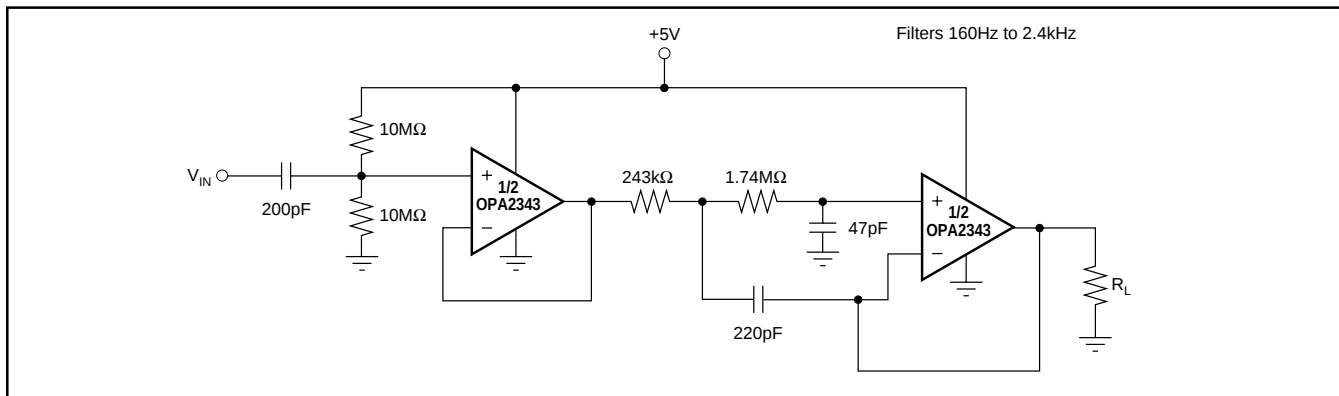


FIGURE 7. Speech Bandpass Filter.

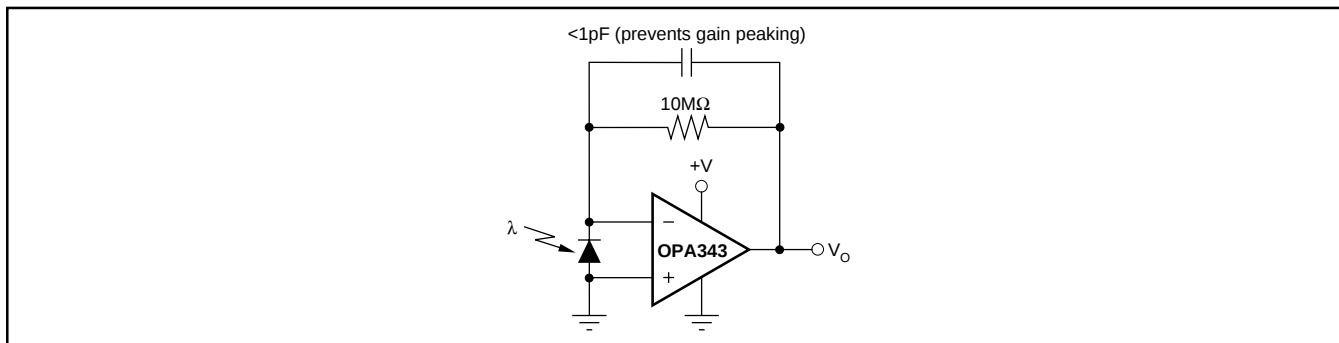


FIGURE 8. Transimpedance Amplifier.

**PACKAGING INFORMATION**

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2) | Lead finish/<br>Ball material<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5) | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|-----------------|--------------------------------------|----------------------|--------------|-------------------------|-------------------------|
| OPA2343EA/250    | ACTIVE        | VSSOP        | DGK                | 8    | 250            | RoHS & Green    | NIPDAUAG                             | Level-2-260C-1 YEAR  | -40 to 85    | C43                     | <a href="#">Samples</a> |
| OPA2343EA/250G4  | ACTIVE        | VSSOP        | DGK                | 8    | 250            | RoHS & Green    | NIPDAUAG                             | Level-2-260C-1 YEAR  | -40 to 85    | C43                     | <a href="#">Samples</a> |
| OPA2343EA/2K5    | ACTIVE        | VSSOP        | DGK                | 8    | 2500           | RoHS & Green    | NIPDAUAG                             | Level-2-260C-1 YEAR  | -40 to 85    | C43                     | <a href="#">Samples</a> |
| OPA2343EA/2K5G4  | ACTIVE        | VSSOP        | DGK                | 8    | 2500           | RoHS & Green    | NIPDAUAG                             | Level-2-260C-1 YEAR  | -40 to 85    | C43                     | <a href="#">Samples</a> |
| OPA2343UA        | ACTIVE        | SOIC         | D                  | 8    | 75             | RoHS & Green    | NIPDAU                               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>2343UA           | <a href="#">Samples</a> |
| OPA2343UA/2K5    | ACTIVE        | SOIC         | D                  | 8    | 2500           | RoHS & Green    | NIPDAU                               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>2343UA           | <a href="#">Samples</a> |
| OPA2343UA/2K5G4  | ACTIVE        | SOIC         | D                  | 8    | 2500           | RoHS & Green    | NIPDAU                               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>2343UA           | <a href="#">Samples</a> |
| OPA343NA/250     | ACTIVE        | SOT-23       | DBV                | 5    | 250            | RoHS & Green    | NIPDAU                               | Level-1-260C-UNLIM   | -40 to 85    | B43                     | <a href="#">Samples</a> |
| OPA343NA/250G4   | ACTIVE        | SOT-23       | DBV                | 5    | 250            | RoHS & Green    | NIPDAU                               | Level-1-260C-UNLIM   | -40 to 85    | B43                     | <a href="#">Samples</a> |
| OPA343NA/3K      | ACTIVE        | SOT-23       | DBV                | 5    | 3000           | RoHS & Green    | NIPDAU                               | Level-1-260C-UNLIM   | -40 to 85    | B43                     | <a href="#">Samples</a> |
| OPA343NA/3KG4    | ACTIVE        | SOT-23       | DBV                | 5    | 3000           | RoHS & Green    | NIPDAU                               | Level-1-260C-UNLIM   | -40 to 85    | B43                     | <a href="#">Samples</a> |
| OPA343UA         | ACTIVE        | SOIC         | D                  | 8    | 75             | RoHS & Green    | NIPDAU                               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>343UA            | <a href="#">Samples</a> |
| OPA343UA/2K5     | ACTIVE        | SOIC         | D                  | 8    | 2500           | RoHS & Green    | NIPDAU                               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>343UA            | <a href="#">Samples</a> |
| OPA343UA/2K5G4   | ACTIVE        | SOIC         | D                  | 8    | 2500           | RoHS & Green    | NIPDAU                               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>343UA            | <a href="#">Samples</a> |
| OPA4343EA/250    | ACTIVE        | SSOP         | DBQ                | 16   | 250            | RoHS & Green    | NIPDAU                               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>4343EA           | <a href="#">Samples</a> |
| OPA4343EA/2K5    | ACTIVE        | SSOP         | DBQ                | 16   | 2500           | RoHS & Green    | NIPDAU                               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>4343EA           | <a href="#">Samples</a> |
| OPA4343NA/250    | ACTIVE        | TSSOP        | PW                 | 14   | 250            | RoHS & Green    | NIPDAU                               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>4343NA           | <a href="#">Samples</a> |
| OPA4343NA/2K5    | ACTIVE        | TSSOP        | PW                 | 14   | 2500           | RoHS & Green    | NIPDAU                               | Level-2-260C-1 YEAR  | -40 to 85    | OPA<br>4343NA           | <a href="#">Samples</a> |

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2) | Lead finish/<br>Ball material<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5) | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|-----------------|--------------------------------------|----------------------|--------------|-------------------------|-------------------------|
| OPA4343UA        | ACTIVE        | SOIC         | D                  | 14   | 50             | RoHS & Green    | NIPDAU                               | Level-2-260C-1 YEAR  | -40 to 85    | OPA4343UA               | <a href="#">Samples</a> |
| OPA4343UA/2K5    | ACTIVE        | SOIC         | D                  | 14   | 2500           | RoHS & Green    | NIPDAU                               | Level-1-260C-UNLIM   | -40 to 85    | OPA4343UA               | <a href="#">Samples</a> |

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt:** TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

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(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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\*All dimensions are nominal

| Device        | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|---------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| OPA2343EA/250 | VSSOP        | DGK             | 8    | 250  | 330.0              | 12.4               | 5.3     | 3.4     | 1.4     | 8.0     | 12.0   | Q1            |
| OPA2343EA/2K5 | VSSOP        | DGK             | 8    | 2500 | 330.0              | 12.4               | 5.3     | 3.4     | 1.4     | 8.0     | 12.0   | Q1            |
| OPA2343UA/2K5 | SOIC         | D               | 8    | 2500 | 330.0              | 12.4               | 6.4     | 5.2     | 2.1     | 8.0     | 12.0   | Q1            |
| OPA343NA/250  | SOT-23       | DBV             | 5    | 250  | 178.0              | 9.0                | 3.23    | 3.17    | 1.37    | 4.0     | 8.0    | Q3            |
| OPA343NA/3K   | SOT-23       | DBV             | 5    | 3000 | 178.0              | 9.0                | 3.23    | 3.17    | 1.37    | 4.0     | 8.0    | Q3            |
| OPA343UA/2K5  | SOIC         | D               | 8    | 2500 | 330.0              | 12.4               | 6.4     | 5.2     | 2.1     | 8.0     | 12.0   | Q1            |
| OPA4343EA/250 | SSOP         | DBQ             | 16   | 250  | 180.0              | 12.4               | 6.4     | 5.2     | 2.1     | 8.0     | 12.0   | Q1            |
| OPA4343EA/2K5 | SSOP         | DBQ             | 16   | 2500 | 330.0              | 12.4               | 6.4     | 5.2     | 2.1     | 8.0     | 12.0   | Q1            |
| OPA4343NA/250 | TSSOP        | PW              | 14   | 250  | 180.0              | 12.4               | 6.9     | 5.6     | 1.6     | 8.0     | 12.0   | Q1            |
| OPA4343NA/2K5 | TSSOP        | PW              | 14   | 2500 | 330.0              | 12.4               | 6.9     | 5.6     | 1.6     | 8.0     | 12.0   | Q1            |
| OPA4343UA/2K5 | SOIC         | D               | 14   | 2500 | 330.0              | 16.4               | 6.5     | 9.0     | 2.1     | 8.0     | 16.0   | Q1            |

## TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

| Device        | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|---------------|--------------|-----------------|------|------|-------------|------------|-------------|
| OPA2343EA/250 | VSSOP        | DGK             | 8    | 250  | 366.0       | 364.0      | 50.0        |
| OPA2343EA/2K5 | VSSOP        | DGK             | 8    | 2500 | 366.0       | 364.0      | 50.0        |
| OPA2343UA/2K5 | SOIC         | D               | 8    | 2500 | 853.0       | 449.0      | 35.0        |
| OPA343NA/250  | SOT-23       | DBV             | 5    | 250  | 180.0       | 180.0      | 18.0        |
| OPA343NA/3K   | SOT-23       | DBV             | 5    | 3000 | 180.0       | 180.0      | 18.0        |
| OPA343UA/2K5  | SOIC         | D               | 8    | 2500 | 853.0       | 449.0      | 35.0        |
| OPA4343EA/250 | SSOP         | DBQ             | 16   | 250  | 210.0       | 185.0      | 35.0        |
| OPA4343EA/2K5 | SSOP         | DBQ             | 16   | 2500 | 853.0       | 449.0      | 35.0        |
| OPA4343NA/250 | TSSOP        | PW              | 14   | 250  | 210.0       | 185.0      | 35.0        |
| OPA4343NA/2K5 | TSSOP        | PW              | 14   | 2500 | 853.0       | 449.0      | 35.0        |
| OPA4343UA/2K5 | SOIC         | D               | 14   | 2500 | 853.0       | 449.0      | 35.0        |

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