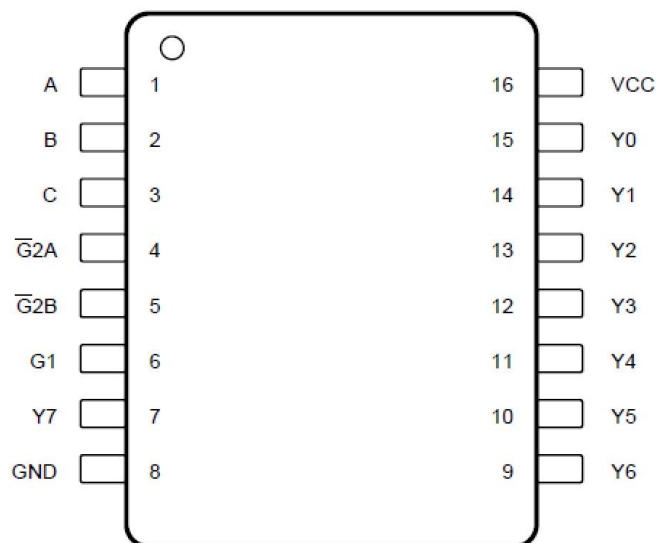## 1. DESCRIPTION

These Schottky-clamped TTL MSI circuits are designed to be used in high-performance memory decoding or data-routing applications requiring very propagation delay times. In high-performance memory systems, these docoders can be used to minimize the effects of system decoding. When employed with high-speed memories utilizing a fast enable circuit,the delay times of the memory are usually less than the typical access time of the memory.This means that the effective system delay introduced by the Schottky-clamped system delay introduced by the Schottky-clamped system decoder is negligible.

The XL/XDLS138 decode one of eight lines dependent on the conditions at the three binary select inputs and the three binary select inputs reduce the need for external gates or inverters when expending.A 24-line decoder can be implemented without external inverters and a 32-line decoder requires only one inverter.An enable input can be used as a data input for demultiplexing applications.
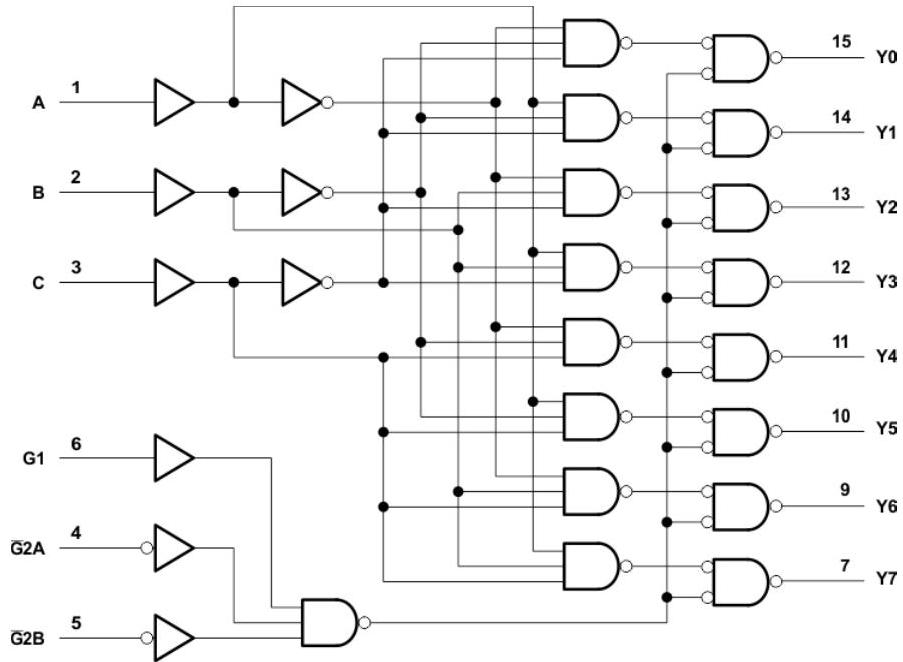
All of these decoder/demultiplexers feature fully buffered inputs,each of which represents only one normalized load to its driving circuit. All inputs are clamped with high-performance Schottky diodes to suppress line-ringing and to simplify system design.

The XL74LS138,XD74LS138 are characterized for operation from 0℃ to 70℃.

2. PIN CONFIGURATIONS

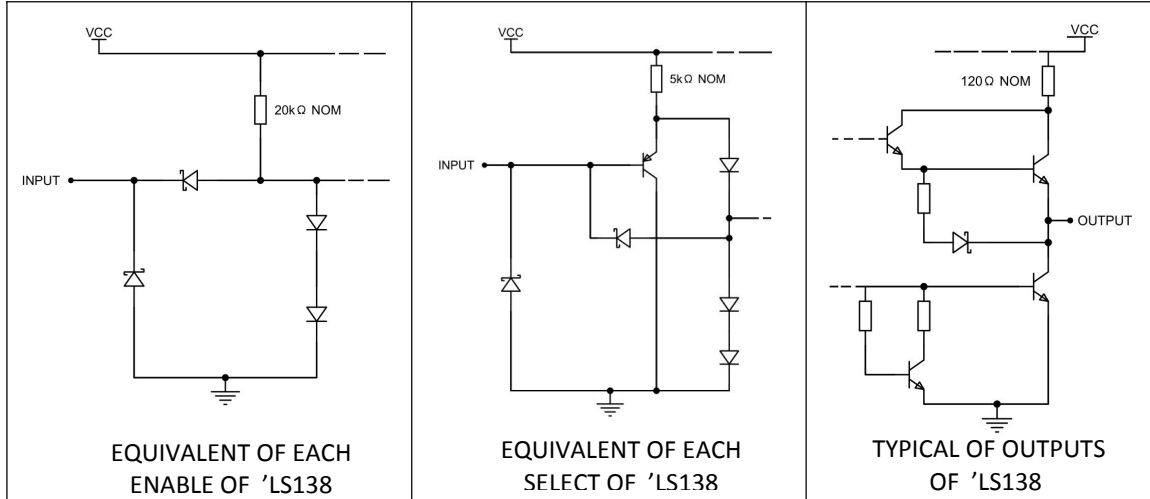| Pin	Name		Pin	Name
1	A		16	VCC
2	B		15	Y0
3	C		14	Y1
4	$\overline{G2A}$		13	Y2
5	$\overline{G2B}$		12	Y3
6	G1		11	Y4
7	Y7		10	Y5
8	GND		9	Y6

3. LOGIC DIAGRAM

INPUTS					OUTPUTS							
ENABLE		SELECT										
$\overline{G1}$	$\overline{G2}$*	C	B	A	Y0	Y1	Y2	Y3	Y4	Y5	Y6	Y7
X	H	X	X	X	H	H	H	H	H	H	H	H
L	X	X	X	X	H	H	H	H	H	H	H	H
H	L	L	L	L	L	H	H	H	H	H	H	H
H	L	L	L	H	H	L	H	H	H	H	H	H
H	L	L	H	L	H	H	L	H	H	H	H	H
H	L	L	H	H	H	H	H	L	H	H	H	H
H	L	H	L	L	H	H	H	H	L	H	H	H
H	L	H	L	H	H	H	H	H	H	L	H	H
H	L	H	H	L	H	H	H	H	H	H	L	H
H	L	H	H	H	H	H	H	H	H	H	H	L

4. SCHEMATICS OF INPUTS AND OUTPUTS

VCC 20kΩ NOM INPUT EQUIVALENT OF EACH ENABLE OF 'LS138	VCC 5kΩ NOM INPUT EQUIVALENT OF EACH SELECT OF 'LS138	VCC 120Ω NOM OUTPUT TYPICAL OF OUTPUTS OF 'LS138

5. ABSOLUTE MAXIMUM RATINGS OVER OPERATING FREE-AIR TEMPERATURE RANGE (UNLESS OTHERWISE NOTES)

Supply voltage, V_{CC} (see Note 1).. 7V

Input voltage, VI: 74LS138.. 7V

Operating free-air temperature range: SOP package... 0°C to 70°C

DIP package... 0°C to 70°C

Storage temperature range, Tstg... −65°C to 150°C

NOTE 1 : Volltage values are with respect to network ground terminal.

6. RECOMMENDED OPERATING CONDITIONS

		XL/XD74LS138			UNIT
		MIN	NOM	MAX	
V_{CC}	Supply voltage	4.75	5	5.25	V
V_{IH}	High-level input voltage	2			V
V_{IL}	Low-level input voltage			0.8	V
I_{OH}	High-level output current			−0.4	mA
I_{OL}	Low-level output current			8	mA
T_A	Operating free-air temperature	0		70	°C

7. ELECTRICAL CHARACTERISTICS OVER RECOMMENDED OPERATING FREE-AIR RANGE (UNLESS OTHERWISE NOTED)

PARAMETER	TEST CONDITIONS†		XL/XD74LS138			UNIT
			MIN	TYP‡	MAX	
V_{IK}	V_{CC} = MIN,	I_I = −18 mA			−1.5	V
V_{OH}	V_{CC} = MIN, V_{IL} = MAX,	I_{OH} = −0.4 mA	2.7	3.4		V
V_{OL}	V_{CC} = MIN, V_{IH} = 2 V	I_{OL} = 4 mA		0.25	0.4	V
		I_{OL} = 8 mA		0.35	0.5	
I_I	V_{CC} = MAX,	V_I = 7 V			0.1	mA
I_{IH}	V_{CC} = MAX,	V_I = 2.7 V			20	μA
I_{IL}	V_{CC} = MAX,	V_I = 0.4 V			−0.4	mA
I_{OS}§	V_{CC} = MAX				−0.2	mA
I_{CCH}	V_{CC} = MAX,	V_I = 4.5 V	−20		−100	mA
I_{CCL}	V_{CC} = MAX,	V_I = 0 V		6.3	10	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.
‡ All typical values are at V_{CC} = 5 V, T_A = 25°C.
§ Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

8. SWITCHING CHARACTERISTICS,VCC = 5 V, TA = 25℃

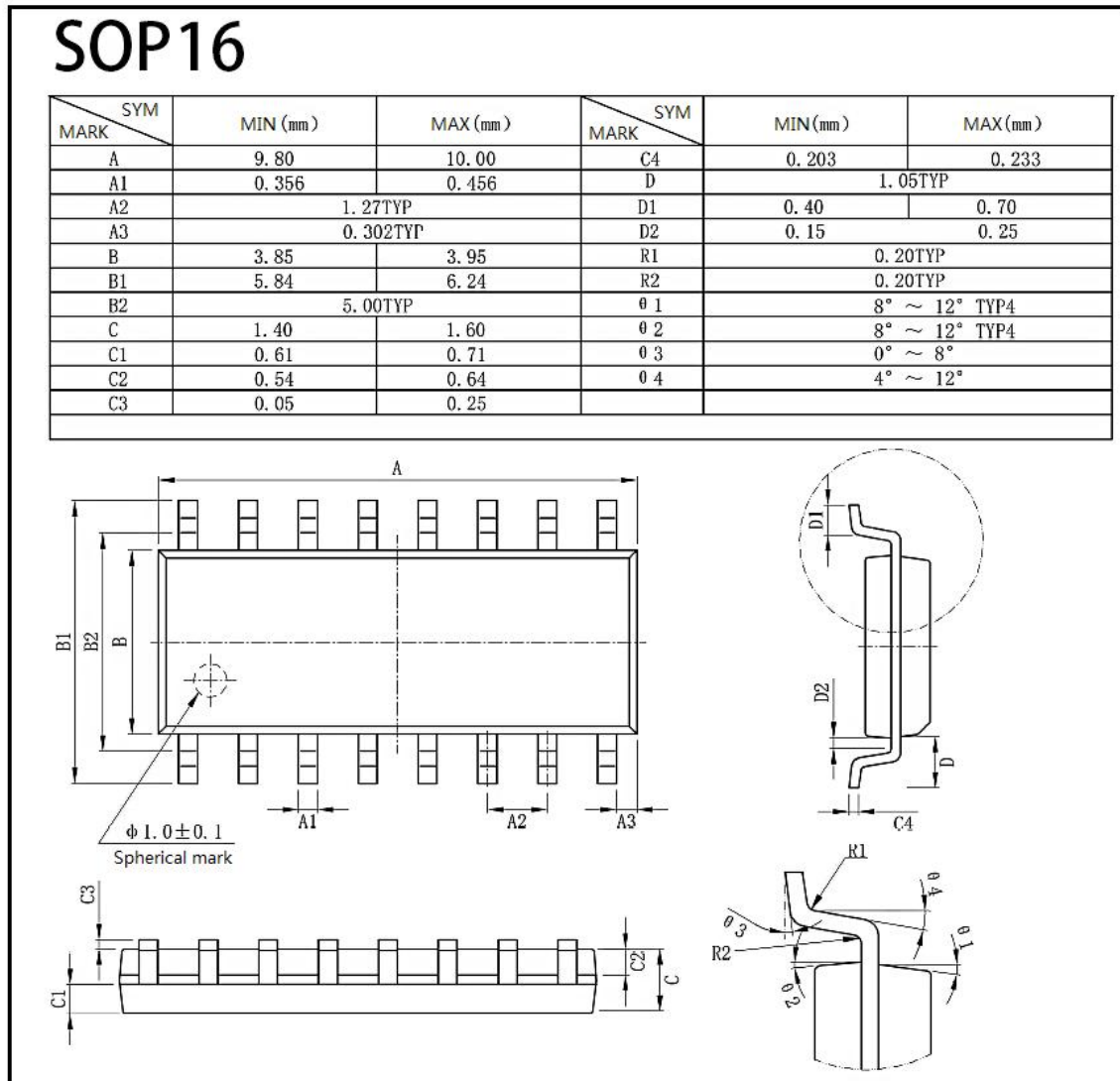| PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	XL/XD74LS138			UNIT
				MIN	TYP	MAX	
t_{PLH}	Binary Select	Any			11	20	ns
t_{PHL}					18	41	ns
t_{PLH}					21	27	ns
t_{PHL}			RL = 2 kΩ, CL = 15 pF See Note 2		20	39	ns
t_{PLH}	Enable	Any			12	18	ns
t_{PHL}					20	32	ns
t_{PLH}					14	26	ns
t_{PHL}					13	38	ns

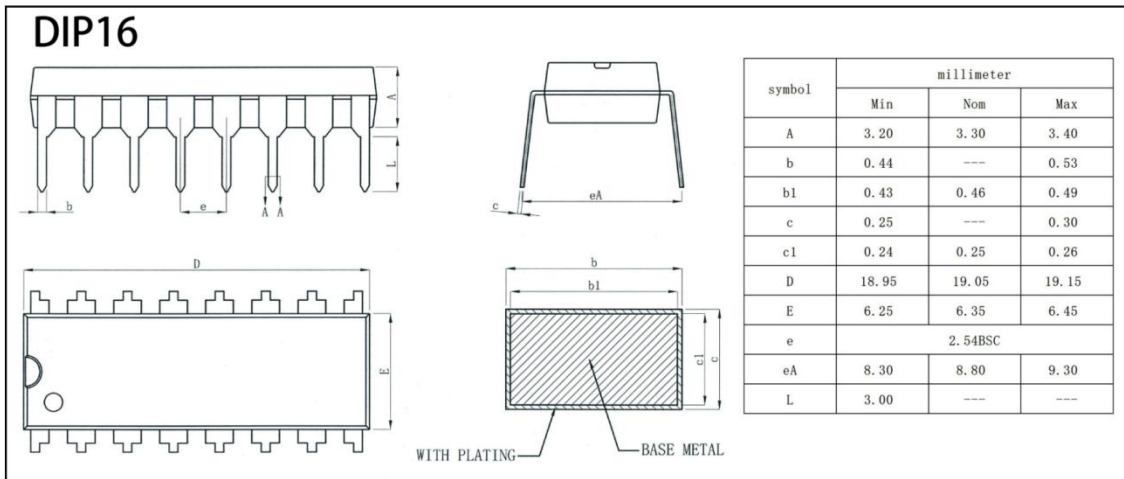NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

9. ORDERING INFORMATION

Ordering Information

Part Number	Device Marking	Package Type	Body size (mm)	Temperature (°C)	MSL	Transport Media	Package Quantity
XL74LS138	XL74LS138	SOP16	10.00 * 3.95	-0 to 70	MSL3	T&R	2500
XD74LS138	XD74LS138	DIP16	19.05 * 6.35	-0 to 70	MSL3	Tube 25	1000

10. DIMENSIONAL DRAWINGS

SOP16

SYM MARK	MIN (mm)	MAX (mm)	SYM MARK	MIN (mm)	MAX (mm)
A	9.80	10.00	C4	0.203	0.233
A1	0.356	0.456	D	1.05TYP	
A2	1.27TYP		D1	0.40	0.70
A3	0.302TYP		D2	0.15	0.25
B	3.85	3.95	R1	0.20TYP	
B1	5.84	6.24	R2	0.20TYP	
B2	5.00TYP		θ1	8° ~ 12° TYP4	
C	1.40	1.60	θ2	8° ~ 12° TYP4	
C1	0.61	0.71	θ3	0° ~ 8°	
C2	0.54	0.64	θ4	4° ~ 12°	
C3	0.05	0.25			

DIP16

WITH PLATING — — BASE METAL

symbol	millimeter		
	Min	Nom	Max
A	3.20	3.30	3.40
b	0.44	---	0.53
b1	0.43	0.46	0.49
c	0.25	---	0.30
c1	0.24	0.25	0.26
D	18.95	19.05	19.15
E	6.25	6.35	6.45
e	2.54BSC		
eA	8.30	8.80	9.30
L	3.00	---	---

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