

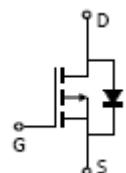
FEATURE

- High density cell design for low $R_{DS(ON)}$
- Voltage controlled small signal switch
- Rugged and reliable
- High saturation current capability

SOT-23

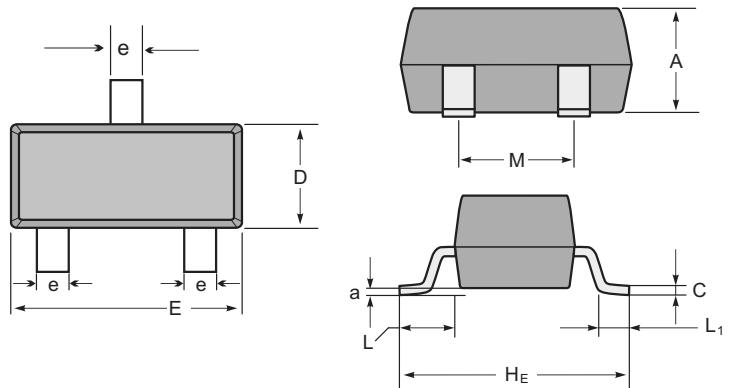


Equivalent Circuit



Marking

Type number	Marking code
SI2301S	A1SHB.



SOT-23 mechanical data

	UNIT	A	C	D	E	H _E	e	M	L	L ₁	a
mm	max	1.1	0.15	1.4	3.0	2.6	0.5	1.95	0.55 (ref)	0.36 (ref)	0.0
	min	0.9	0.08	1.2	2.8	2.2	0.3	1.7			0.15
mil	max	43	6	55	118	102	20	77	22 (ref)	14 (ref)	0.0
	min	35	3	47	110	87	12	67			6

Maximum ratings ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 8	
Continuous Drain Current	I_D	-2.3	A
Pulsed Drain Current	I_{DM}	-10	
Continuous Source-Drain Diode Current	I_S	-0.72	
Maximum Power Dissipation	P_D	1.0	W
Thermal Resistance from Junction to Ambient($t \leq 5\text{s}$)	$R_{\theta JA}$	357	$^\circ\text{C/W}$
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 ~ +150	

SI2301S

Electrical characteristics ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Static						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = -250\mu\text{A}$	-20			V
Gate-source threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = -250\mu\text{A}$	-0.4		-1	
Gate-source leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 8\text{V}$			± 100	nA
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = -20\text{V}, V_{\text{GS}} = 0\text{V}$			-1	μA
Drain-source on-state resistance ^a	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -4.5\text{V}, I_{\text{D}} = -2.8\text{A}$		0.090	0.112	Ω
		$V_{\text{GS}} = -2.5\text{V}, I_{\text{D}} = -2.0\text{A}$		0.110	0.142	
Forward transconductance ^a	g_{fs}	$V_{\text{DS}} = -5\text{V}, I_{\text{D}} = -2.8\text{A}$		6.5		S
Dynamic ^b						
Input capacitance	C_{iss}	$V_{\text{DS}} = -10\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		405		pF
Output capacitance	C_{oss}			75		
Reverse transfer capacitance	C_{rss}			55		
Total gate charge	Q_g	$V_{\text{DS}} = -10\text{V}, V_{\text{GS}} = -4.5\text{V}, I_{\text{D}} = -3\text{A}$		5.5	10	nC
Gate-source charge	Q_{gs}			3.3	6	
Gate-drain charge	Q_{gd}			0.7		
Gate resistance	R_g			1.3		
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = -10\text{V}, R_L = 10\Omega, I_{\text{D}} = -1\text{A}, V_{\text{GEN}} = -4.5\text{V}, R_{\text{g}} = 1\Omega$		6.0		Ω
Rise time	t_r			11	20	ns
Turn-off delay time	$t_{\text{d}(\text{off})}$			35	60	
Fall time	t_f			30	50	
Drain-source body diode characteristics						
Continuous source-drain diode current	I_s	$T_c = 25^\circ\text{C}$			-1.3	A
Pulse diode forward current ^a	I_{SM}				-10	
Body diode voltage	V_{SD}	$I_s = -0.7\text{A}$		-0.8	-1.2	V

Notes :

a.Pulse Test : Pulse Width < 300μs, Duty Cycle ≤2%.

b.Guaranteed by design, not subject to production testing.

RATING AND CHARACTERISTIC CURVES (SI2301S)

