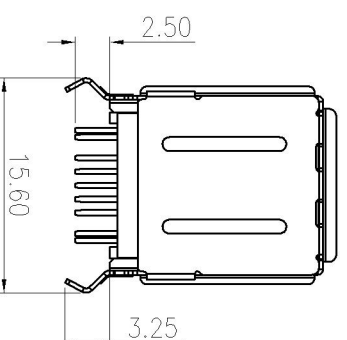
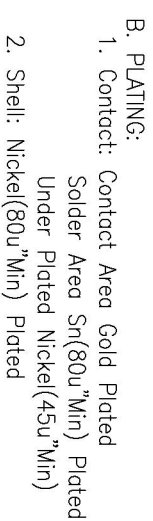


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PIN NO.	10	11	12	13	14	15	16	17	18
PIN NO.	1	2	3	4	5	6	7	8	9
SIGNAL NAME	VBIS	D-	D+	QND	Stda_SRR+	Stda_SRR+	QND-DPMN	Stda_SRR+	Stda_SRR+

PIN NO.	10	11	12	13	14	15	16	17	18
PIN NO.	1	2	3	4	5	6	7	8	9
SIGNAL NAME	VBIS	D-	D+	QND	Stda_SRR+	Stda_SRR+	QND-DPMN	Stda_SRR+	Stda_SRR+

[illegible]