

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

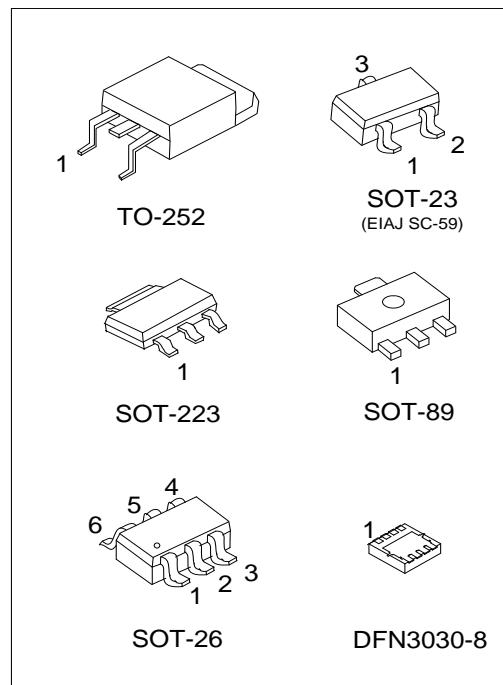
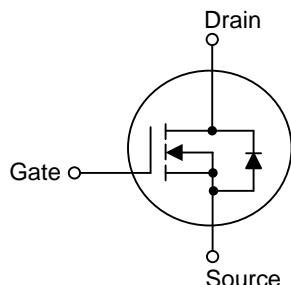
■ DESCRIPTION

The UTC **UT3N10** is an N-channel power MOSFET providing very low on-resistance. It has high efficiency and perfect cost-effectiveness. It can be generally applied in the commercial and industrial fields.

■ FEATURES

- * $R_{DS(ON)} \leq 0.165 \Omega$ @ $V_{GS}=10V$, $I_D=3.0A$
- $R_{DS(ON)} \leq 0.180 \Omega$ @ $V_{GS}=4.5V$, $I_D=2.0A$
- * Simple drive requirement

■ SYMBOL



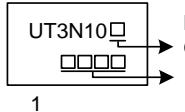
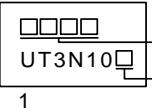
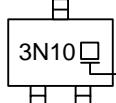
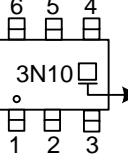
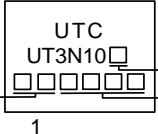
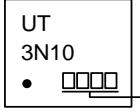
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT3N10L-AA3-R	UT3N10G-AA3-R	SOT-223	G	D	S	-	-	-	-	-	Tape Reel
UT3N10L-AB3-R	UT3N10G-AB3-R	SOT-89	G	D	S	-	-	-	-	-	Tape Reel
UT3N10L-AE3-R	UT3N10G-AE3-R	SOT-23	G	S	D	-	-	-	-	-	Tape Reel
UT3N10L-AG6-R	UT3N10G-AG6-R	SOT-26	D	D	G	S	D	D	-	-	Tape Reel
UT3N10L-TN3-R	UT3N10G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
UT3N10L-K08-3030-R	UT3N10G-K08-3030-R	DFN3030-8	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

 UT3N10G-AA3-R	(1) R: Tape Reel
	(2) AA3: SOT-223, AB3: SOT-89, AE3: SOT-23
	AG6: SOT-26, TN3: TO-252
	K08-3030: DFN3030-8
	(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING

SOT-223	SOT-89
 1	 1
SOT-23	SOT-26
 L: Lead Free G: Halogen Free	 6 5 4 1 2 3 L: Lead Free G: Halogen Free
TO-252	DFN3030-8
 1 Lot Code L: Lead Free G: Halogen Free Date Code	 • Date Code

■ **ABSOLUTE MAXIMUM RATINGS** ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Drain-Source Voltage	V_{DSS}	100	V	
Gate-Source Voltage	V_{GSS}	± 20	V	
Continuous Drain Current ($V_{GS}=4.5\text{V}$, $T_A= 25^\circ\text{C}$) (Note 2)	I_D	3.0	A	
Pulsed Drain Current (Note 3, 4)	I_{DM}	10	A	
Power Dissipation ($T_A= 25^\circ\text{C}$)	SOT-223	P_D	0.89	W
	SOT-89		0.55	W
	SOT-23		0.35	W
	SOT-26		2	W
	TO-252		0.96	W
	DFN3030-8		+150	°C
Junction Temperature	T_J	-55 ~ +150	°C	
Storage Temperature	T_{STG}			

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Surface mounted on 1 in² copper pad of FR4 board; 270°C/W when mounted on min. copper pad.

3. Repetitive Rating: Pulse width limited by maximum junction temperature.

4. Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

■ **THERMAL DATA**

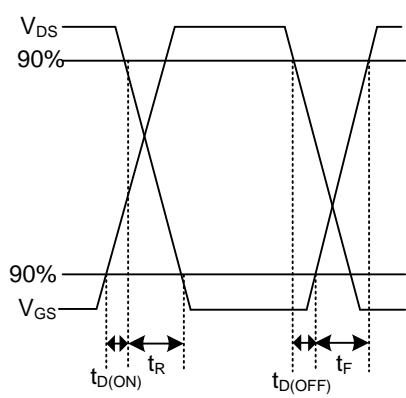
PARAMETER	SYMBOL	RATING	UNIT	
Junction to Ambient (Note)	SOT-223	θ_{JA}	140	°C/W
	SOT-89		180	°C/W
	SOT-23		350	°C/W
	SOT-26		62.5	°C/W
	TO-252		130	°C/W
	DFN3030-8			

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

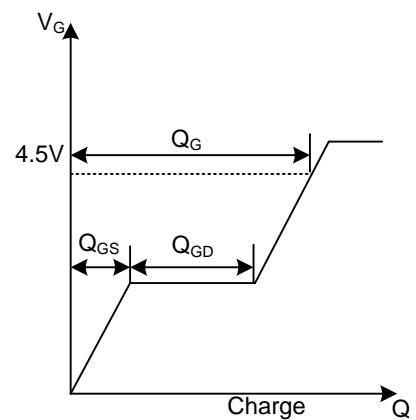
■ **ELECTRICAL CHARACTERISTICS** ($T_J = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 250\mu\text{A}$	100			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}} = 100\text{V}, V_{\text{GS}} = 0\text{V}$			10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}$			± 100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250\mu\text{A}$	1.0		3.0	V
Drain to Source On-state Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 3.0\text{A}$			0.165	Ω
		$V_{\text{GS}} = 4.5\text{V}, I_{\text{D}} = 2.0\text{A}$			0.180	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{\text{DS}} = 25\text{V}, V_{\text{GS}} = 0\text{V}, f = 1.0\text{MHz}$		720		pF
Output Capacitance	C_{OSS}			45		pF
Reverse Transfer Capacitance	C_{RSS}			36		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note)	Q_G	$V_{\text{GS}} = 10\text{V}, V_{\text{DS}} = 80\text{V}, I_{\text{D}} = 3\text{A}$		20		nC
Gate Source Charge	Q_{GS}			3.2		nC
Gate Drain Charge	Q_{GD}			4.2		nC
Turn-ON Delay Time (Note)	$t_{\text{D}(\text{ON})}$	$V_{\text{GS}} = 10\text{V}, V_{\text{DS}} = 50\text{V}, I_{\text{D}} = 3\text{A}, R_G = 25\Omega$		8		ns
Turn-ON Rise Time	t_R			18		ns
Turn-OFF Delay Time	$t_{\text{D}(\text{OFF})}$			75		ns
Turn-OFF Fall-Time	t_F			30		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Drain-Source Diode Forward Voltage (Note)	V_{SD}	$I_S = 1.2\text{A}, V_{\text{GS}} = 0\text{V}$			1.2	V
Reverse Recovery Time	t_{rr}	$I_S = 3\text{A}, V_{\text{GS}} = 0\text{V}, dI/dt = 100\text{A}/\mu\text{s}$		50		ns
Reverse Recovery Charge	Q_{rr}			140		nC

Note: Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

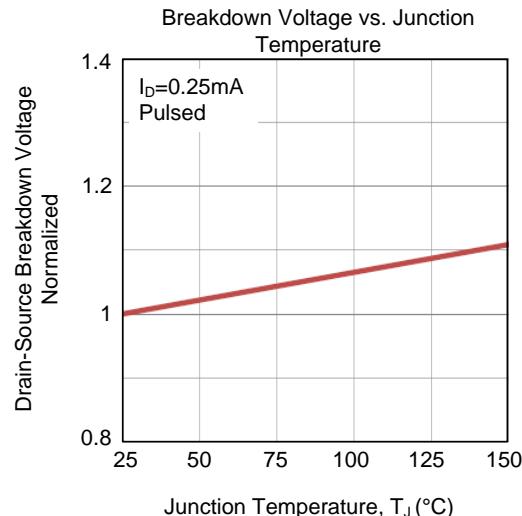
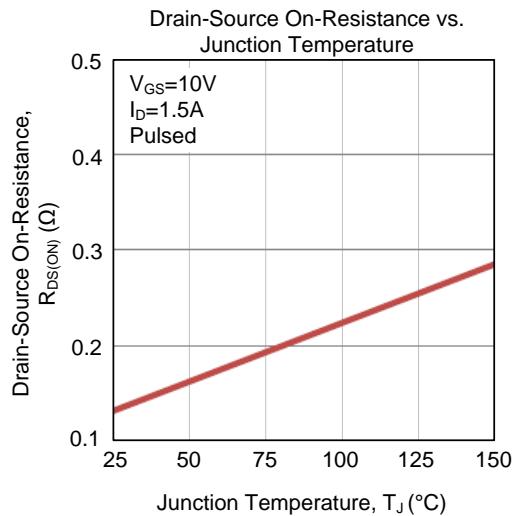
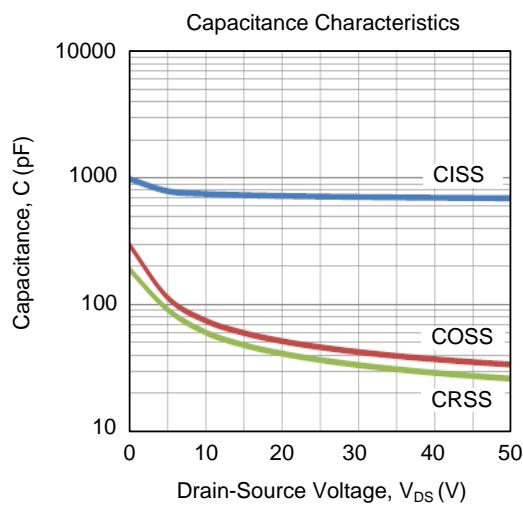
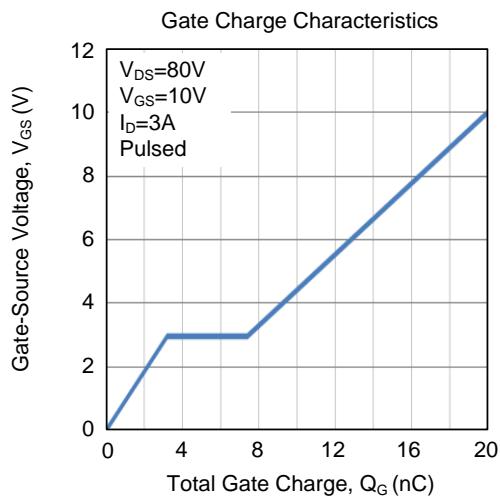
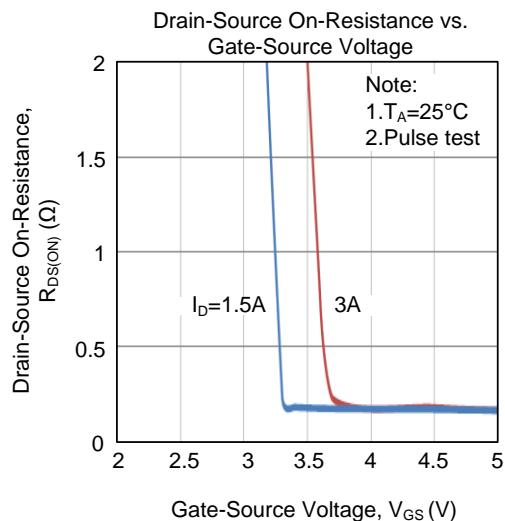
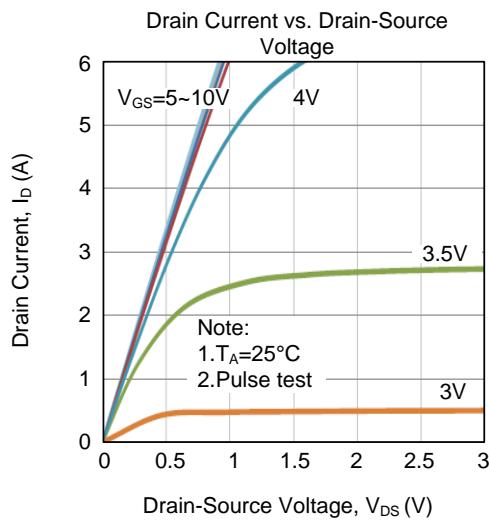
■ TEST WAVEFORMS

Switching Time Waveform

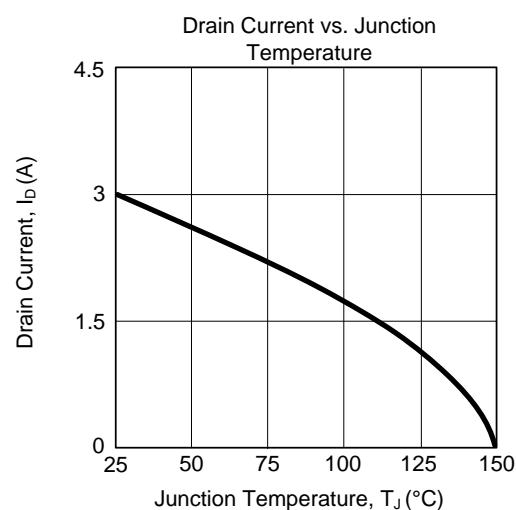
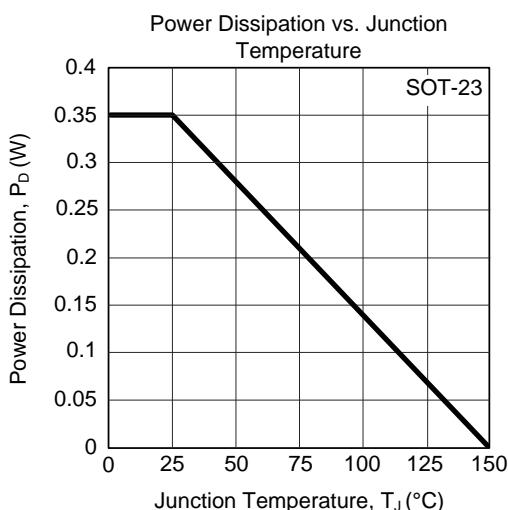
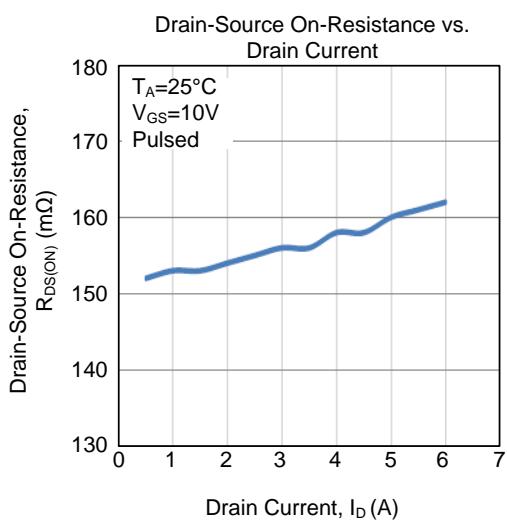
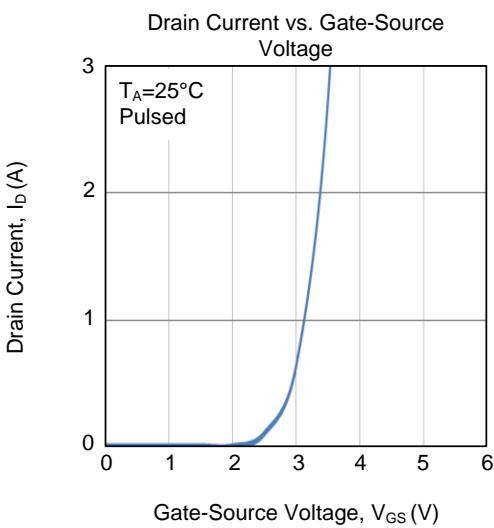
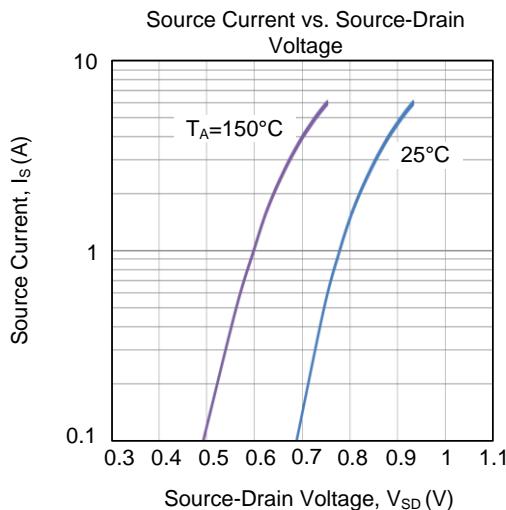
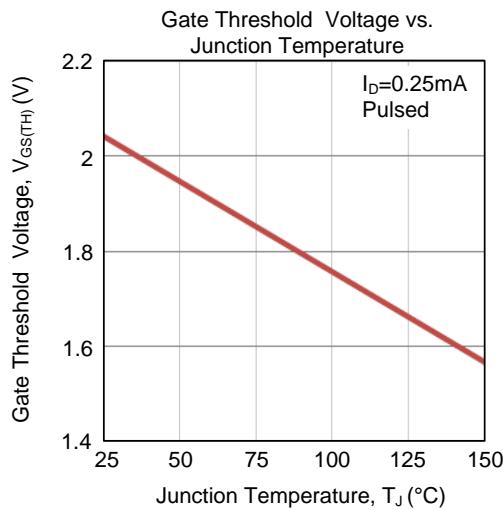


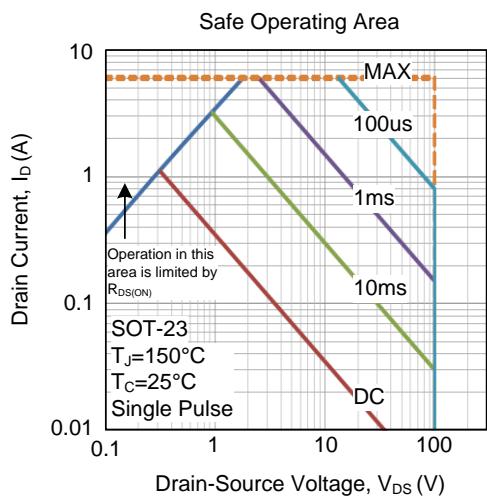
Gate Charge Waveform

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)

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